

FIG. 1A

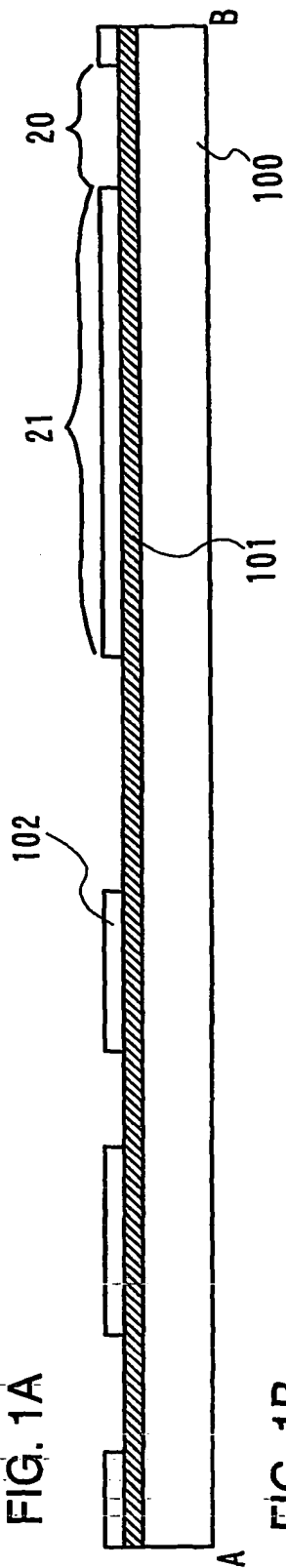


FIG. 1B

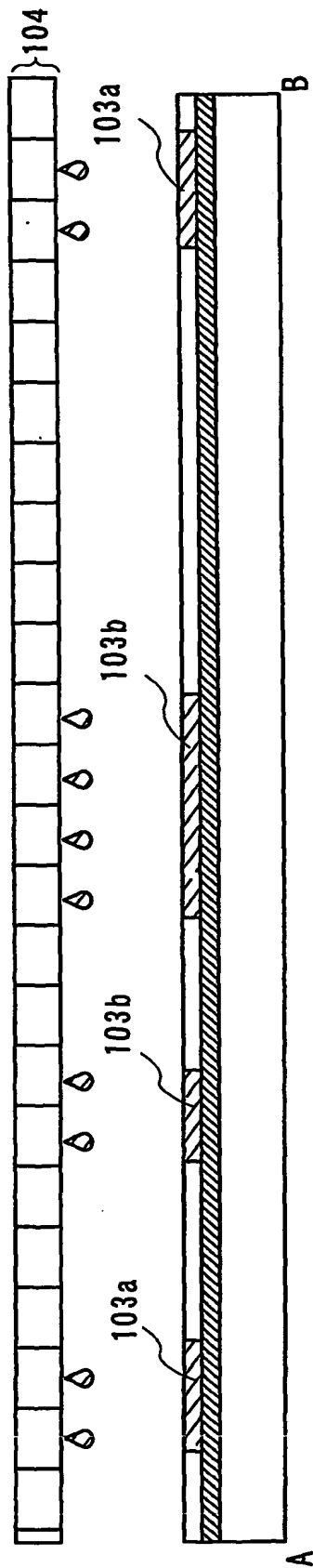


FIG. 1C

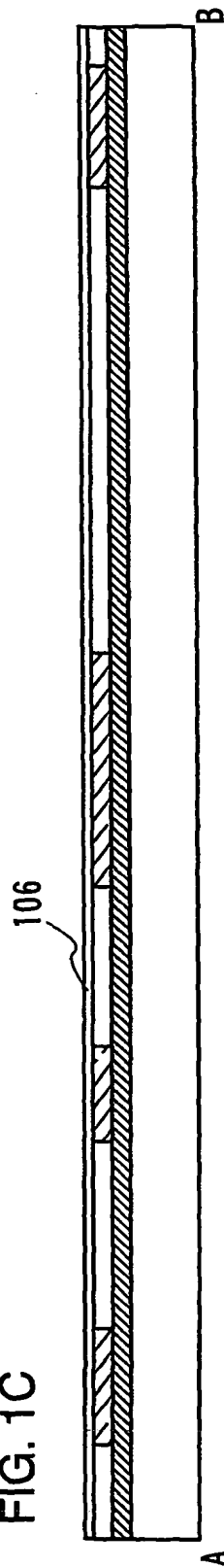


FIG. 1D

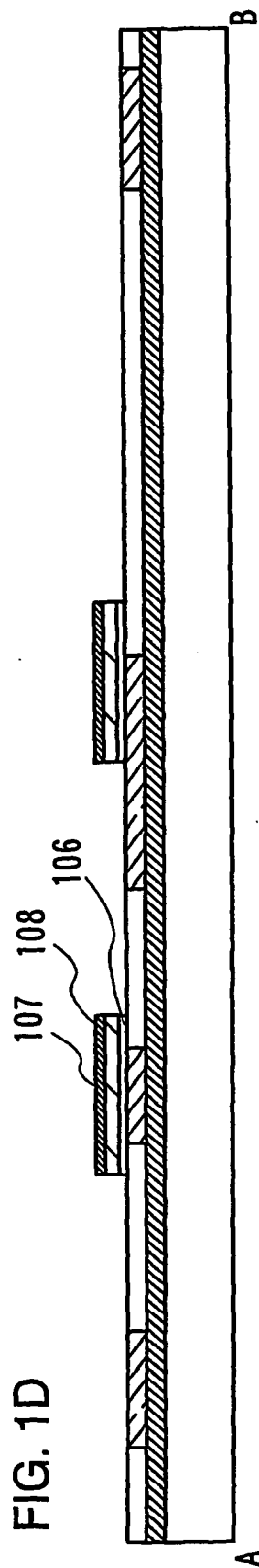


FIG. 2A

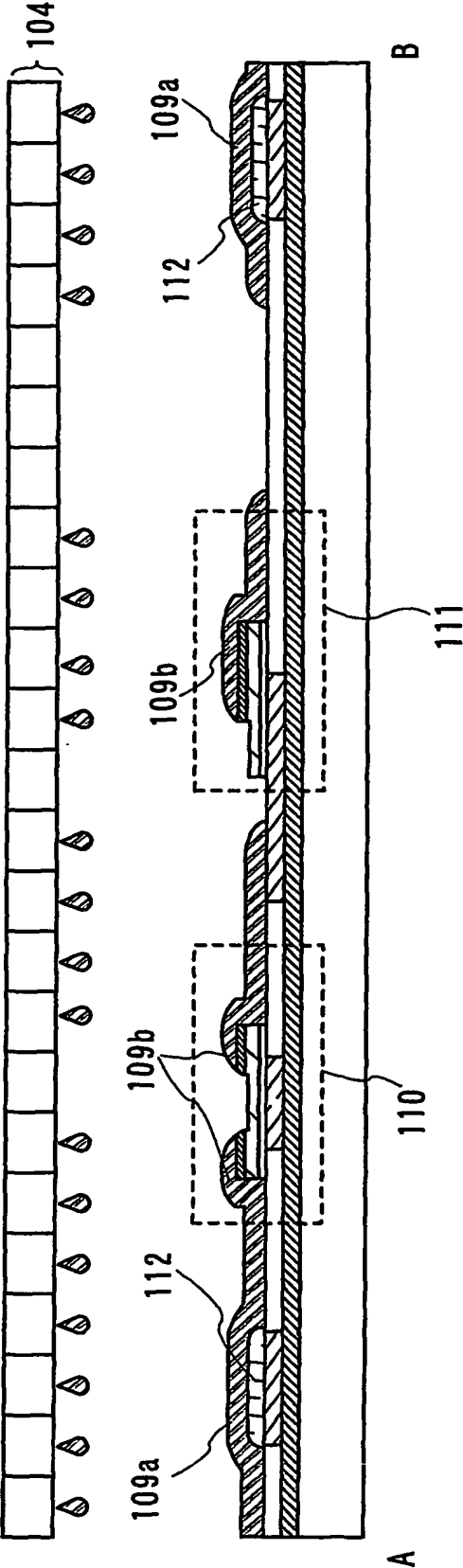


FIG. 2B

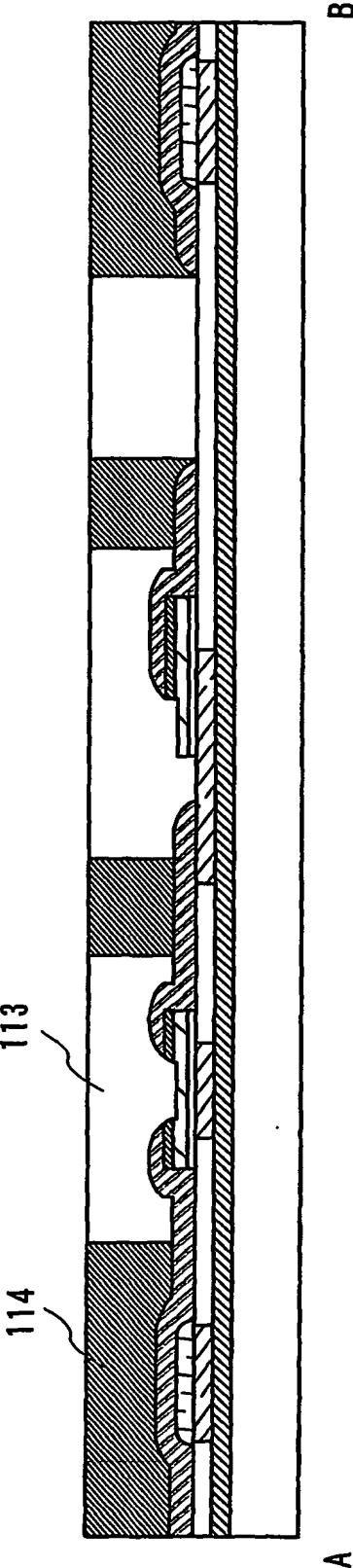


FIG. 3A

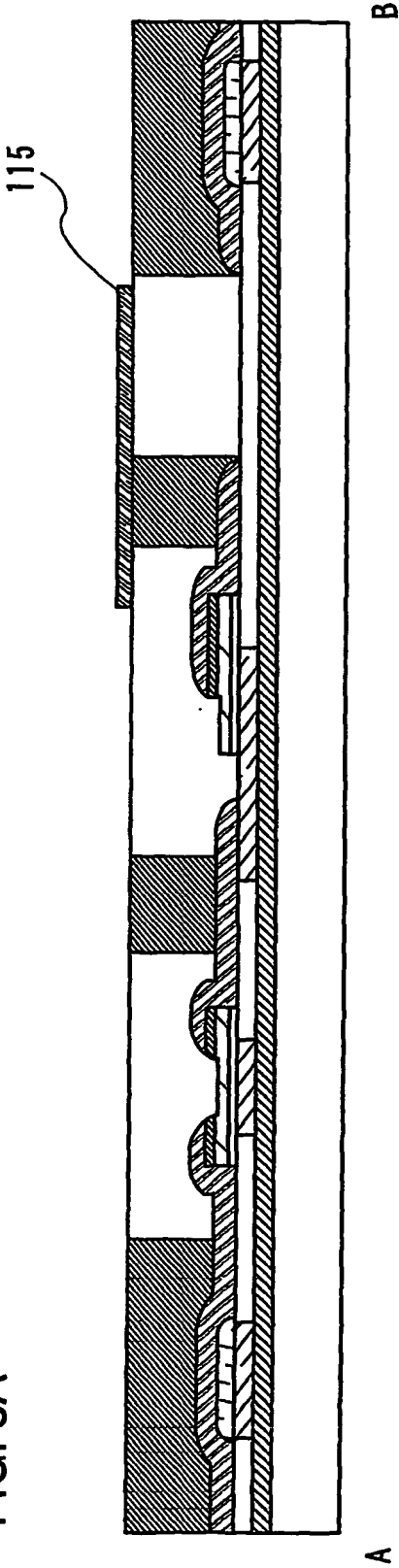
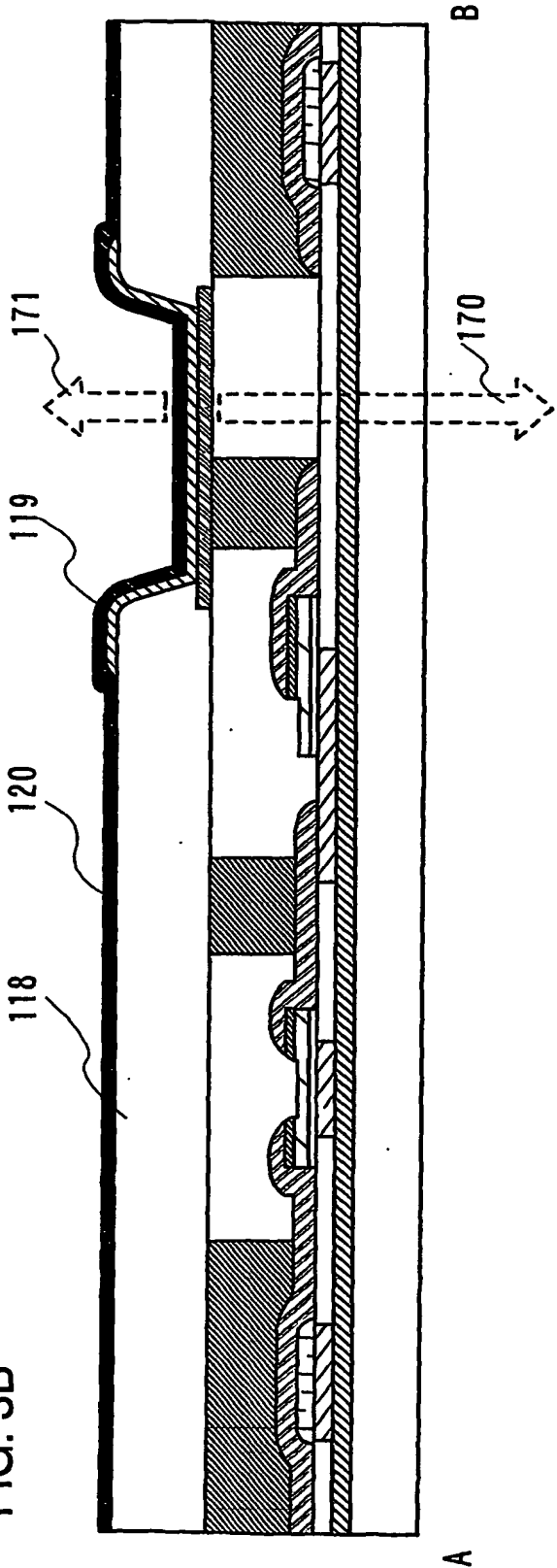


FIG. 3B



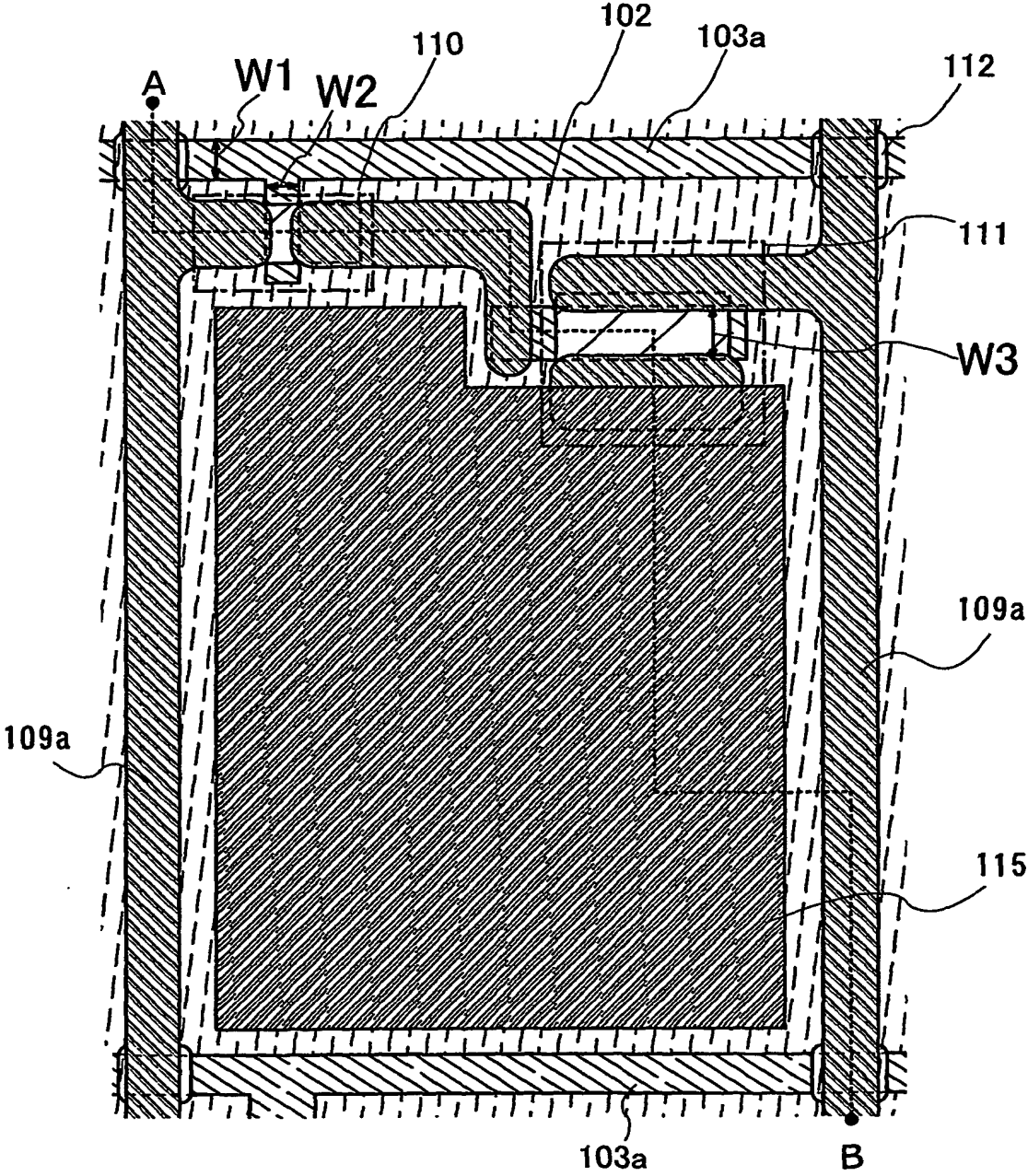


FIG. 4

FIG. 5A

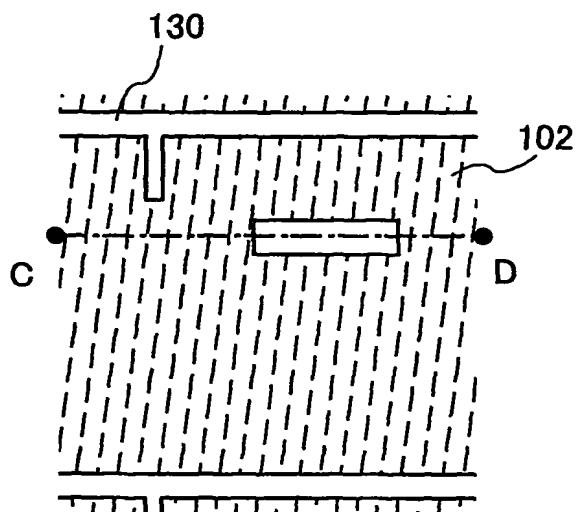


FIG. 5B

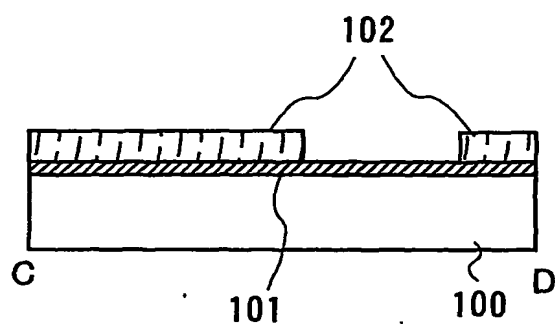


FIG. 5C

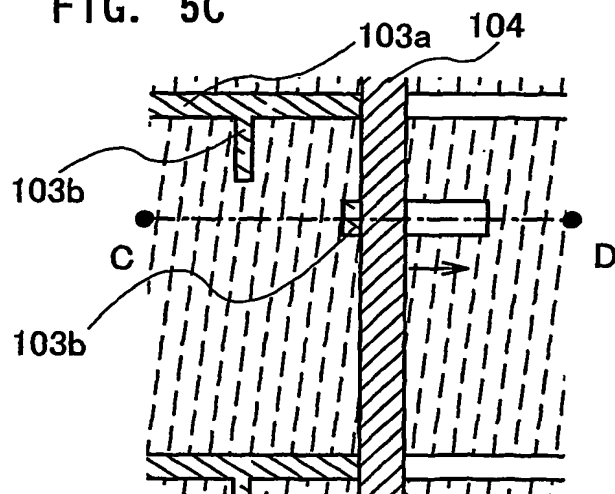
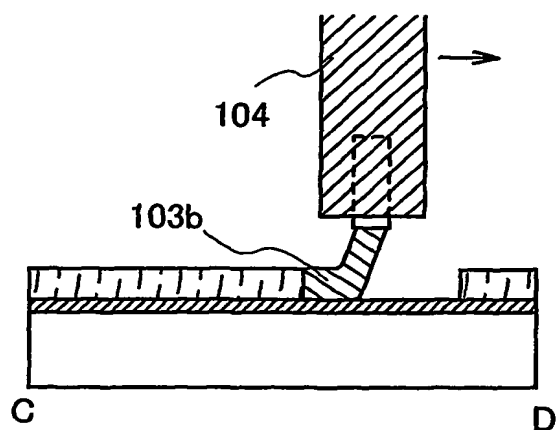


FIG. 5D



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FIG. 6A

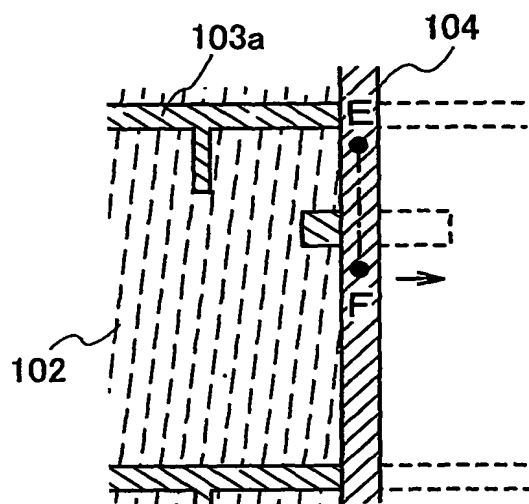


FIG. 6B

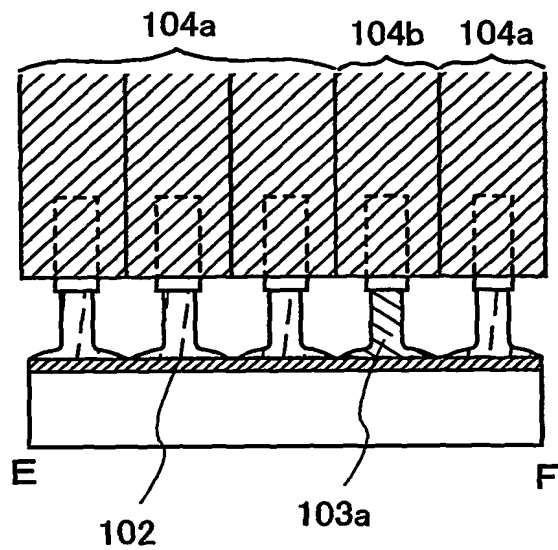


FIG. 7A

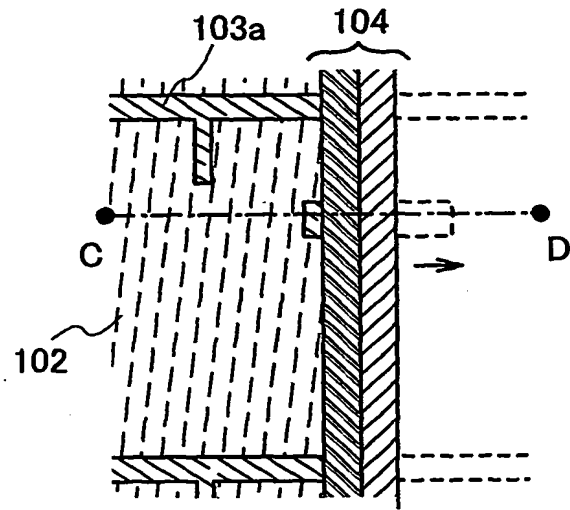


FIG. 7B

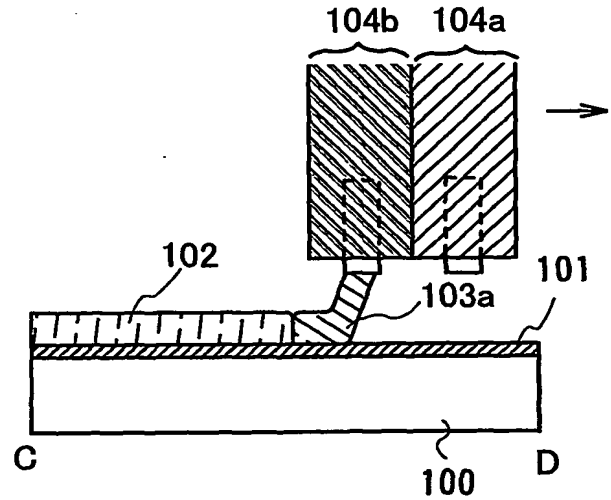


FIG. 8A

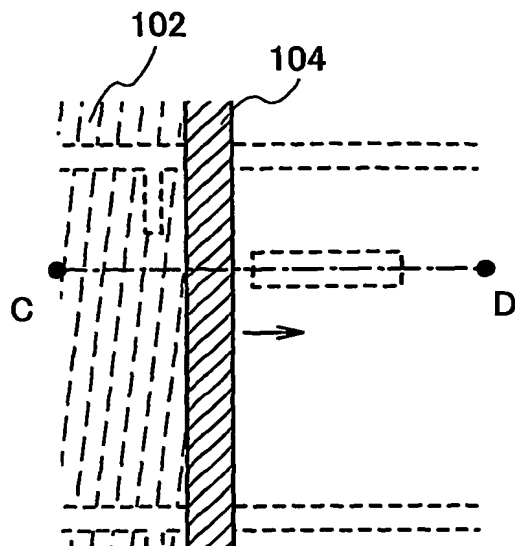


FIG. 8B

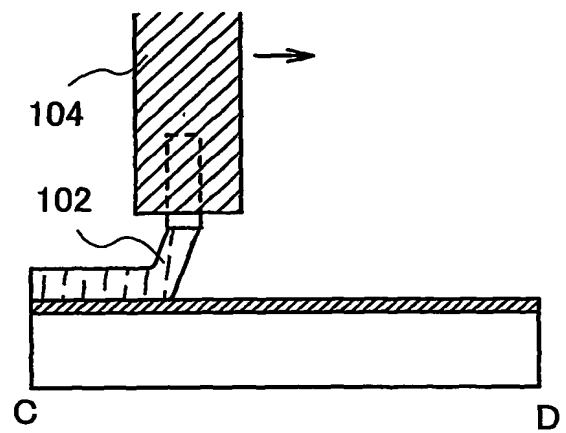


FIG. 8C

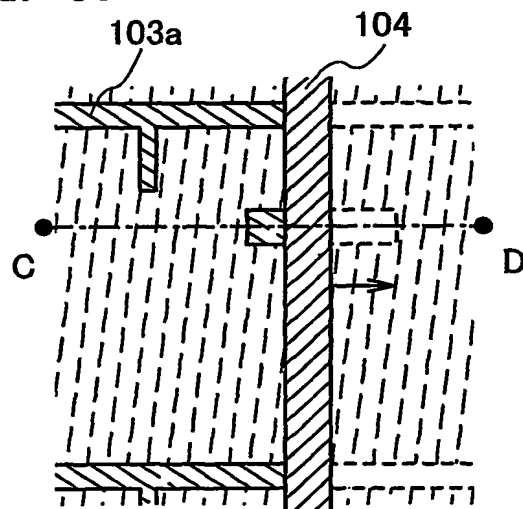


FIG. 8D

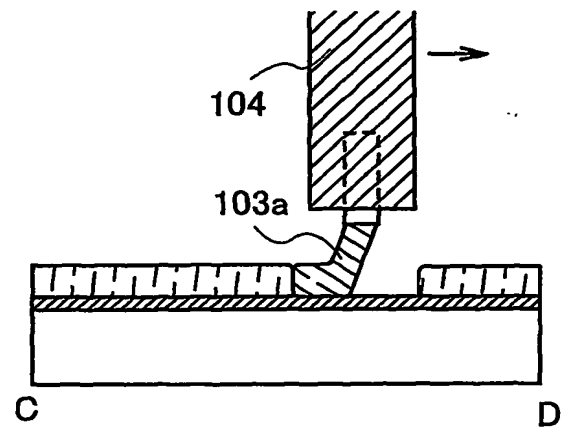


FIG. 9A

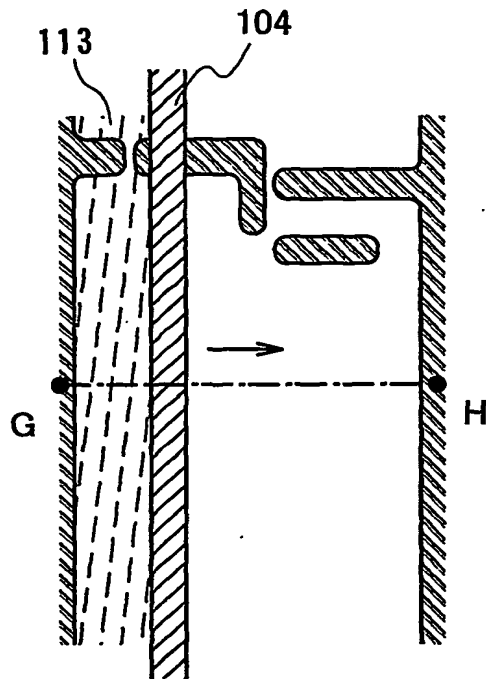


FIG. 9B

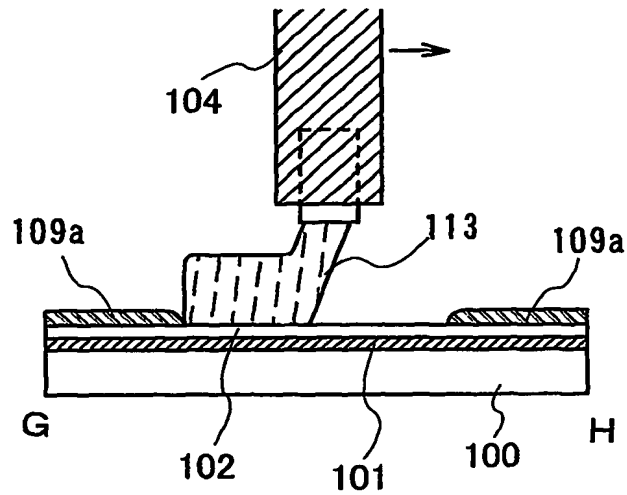


FIG. 9C

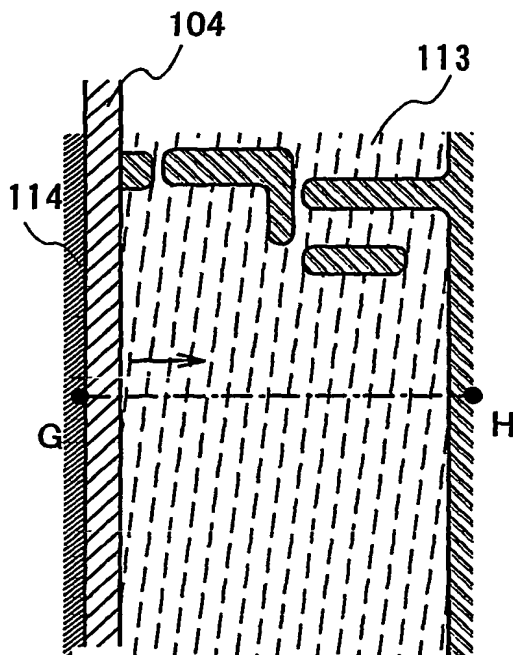
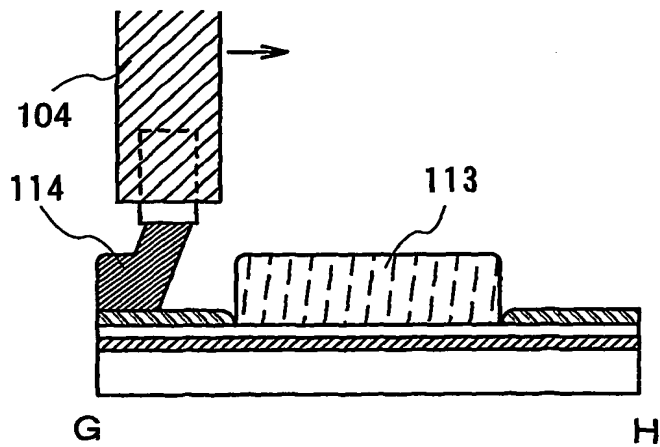


FIG. 9D



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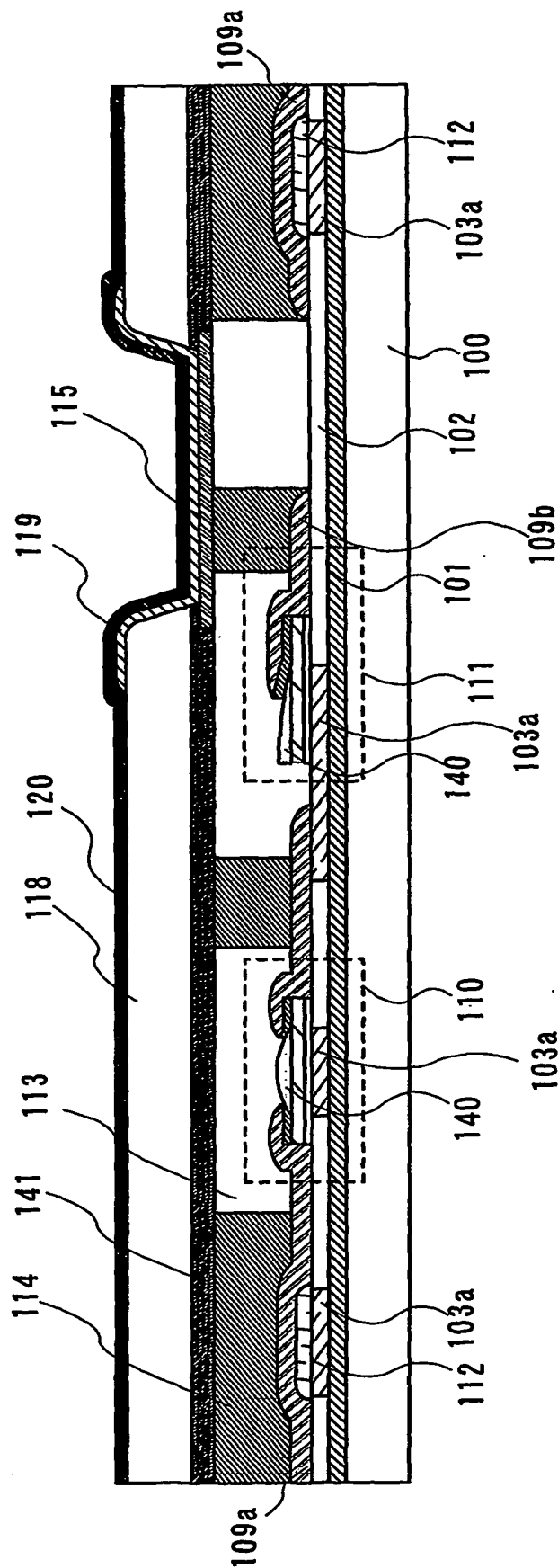


FIG. 10

FIG. 11A

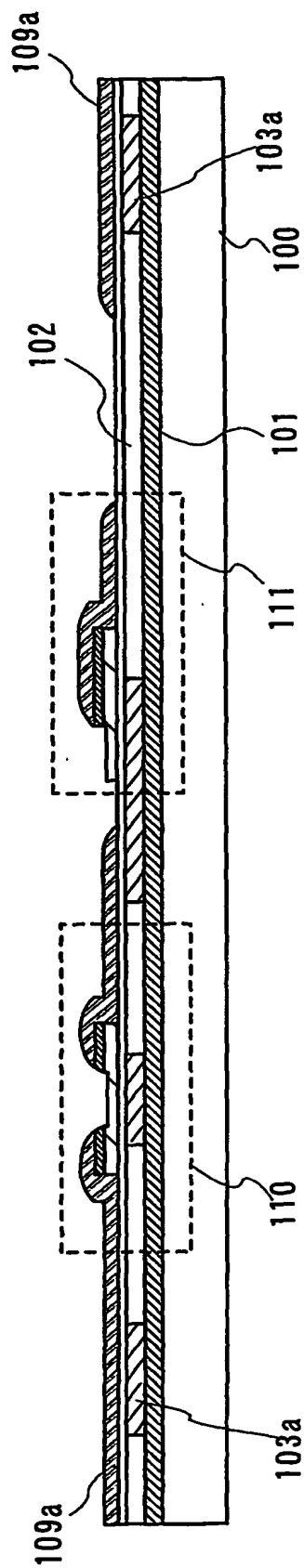
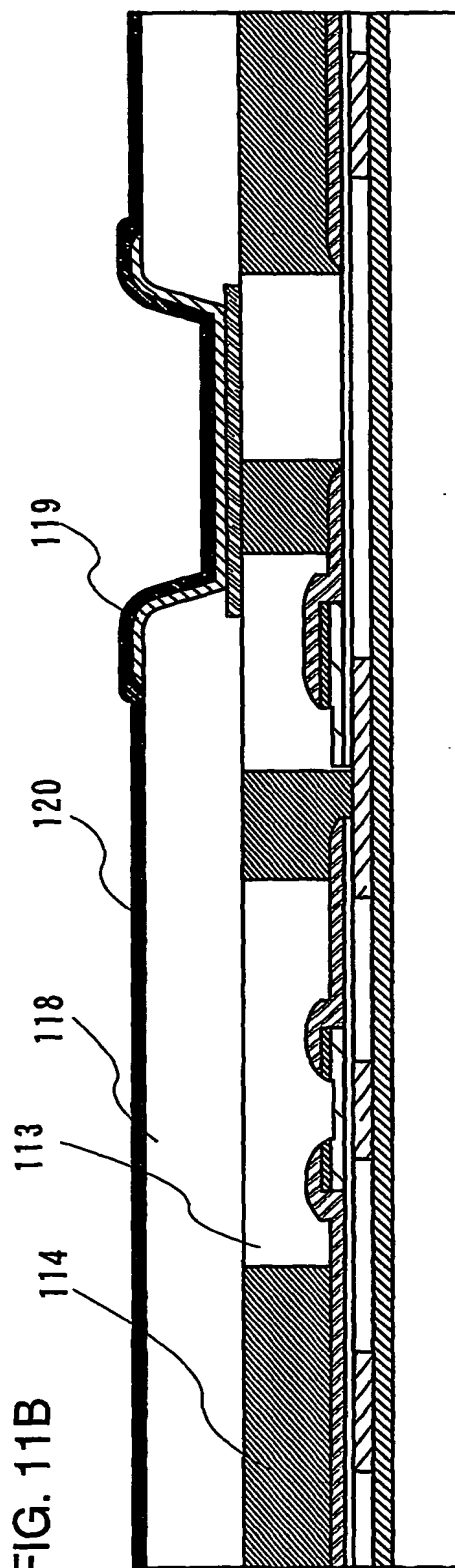


FIG. 11B



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FIG. 12A

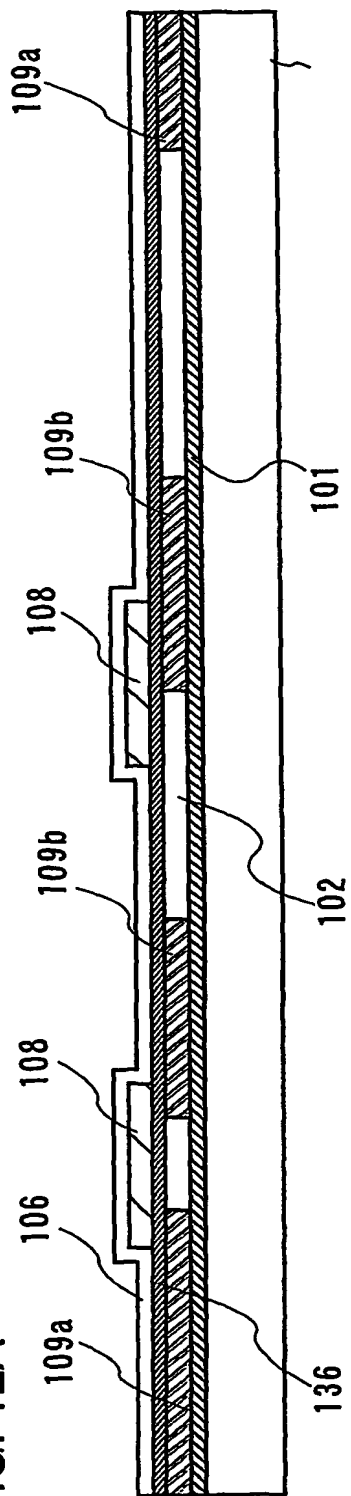


FIG. 12B

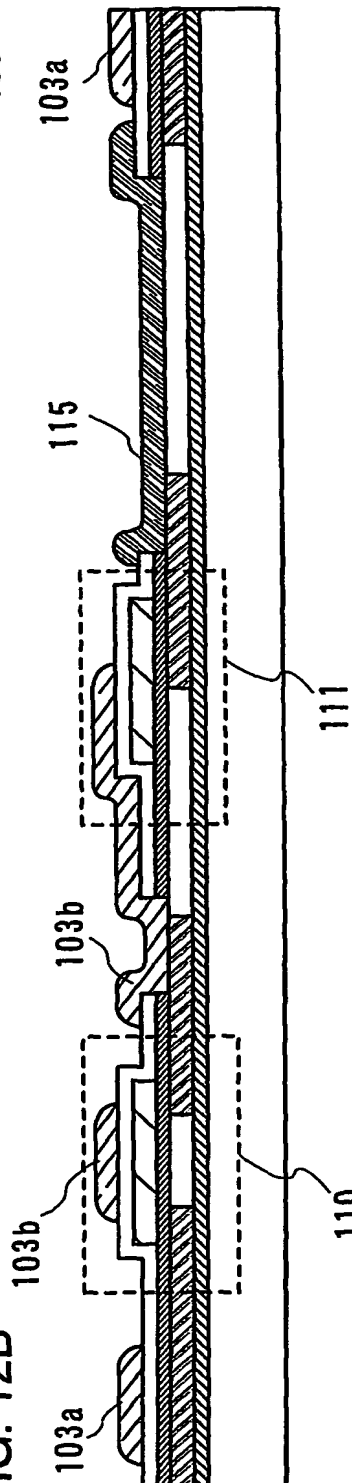


FIG. 12C

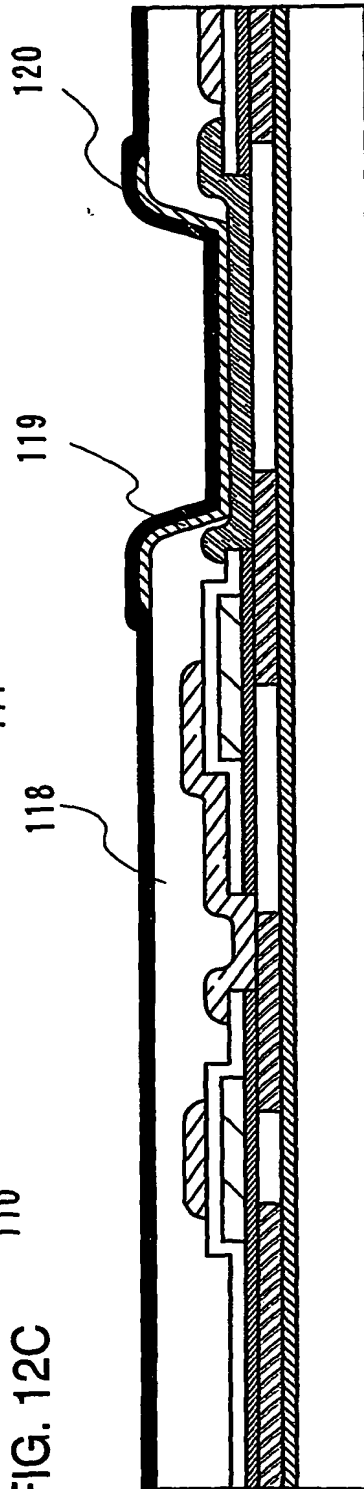


FIG. 13A

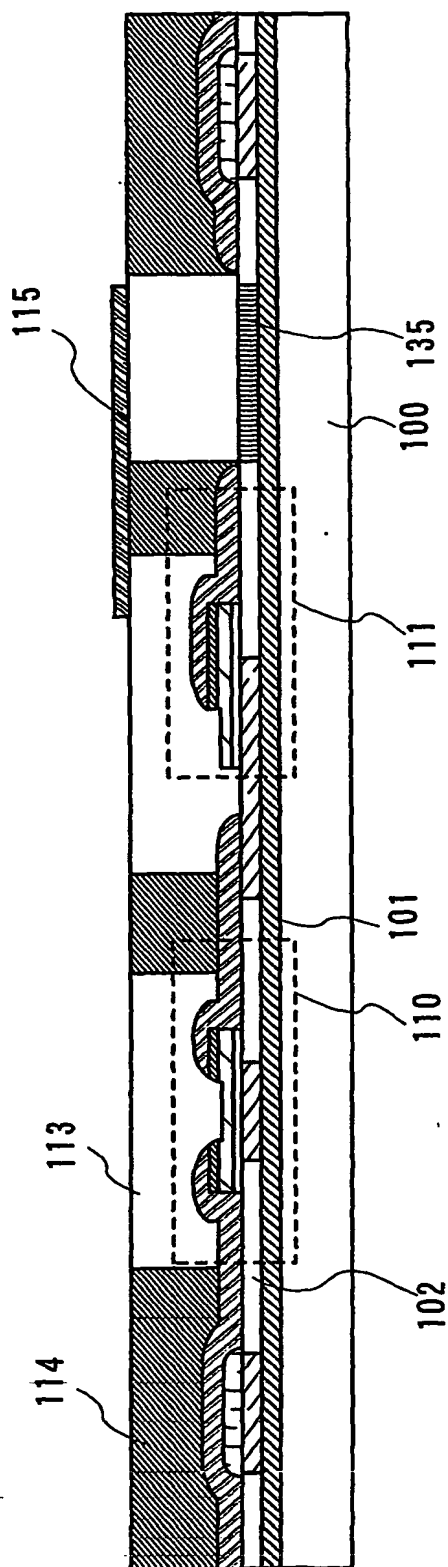


FIG. 13B

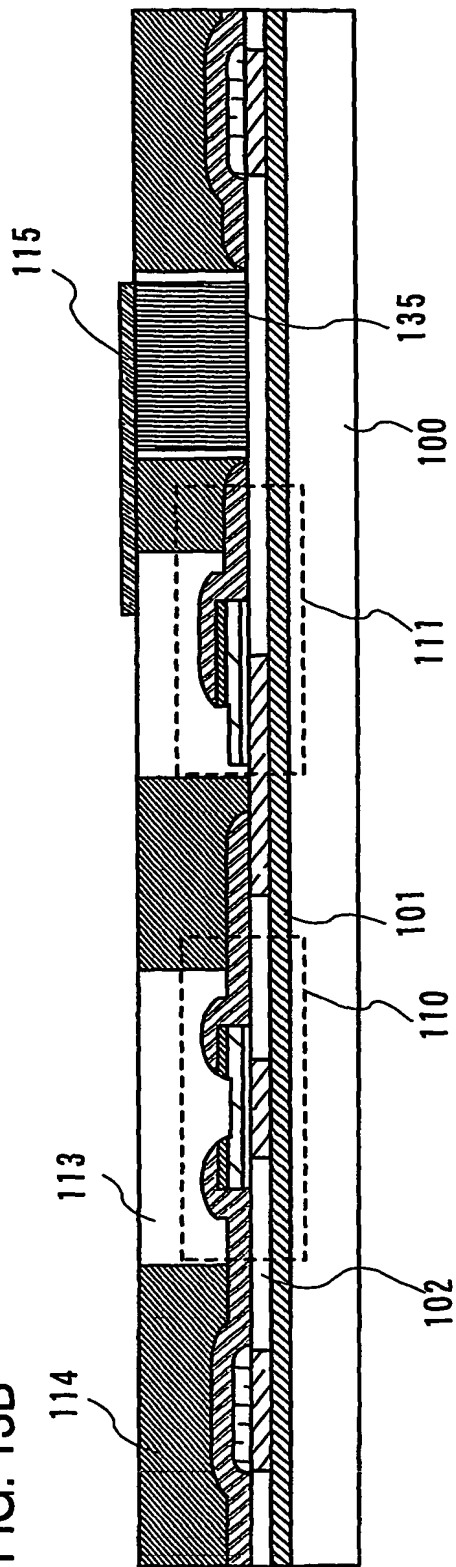


FIG. 14A

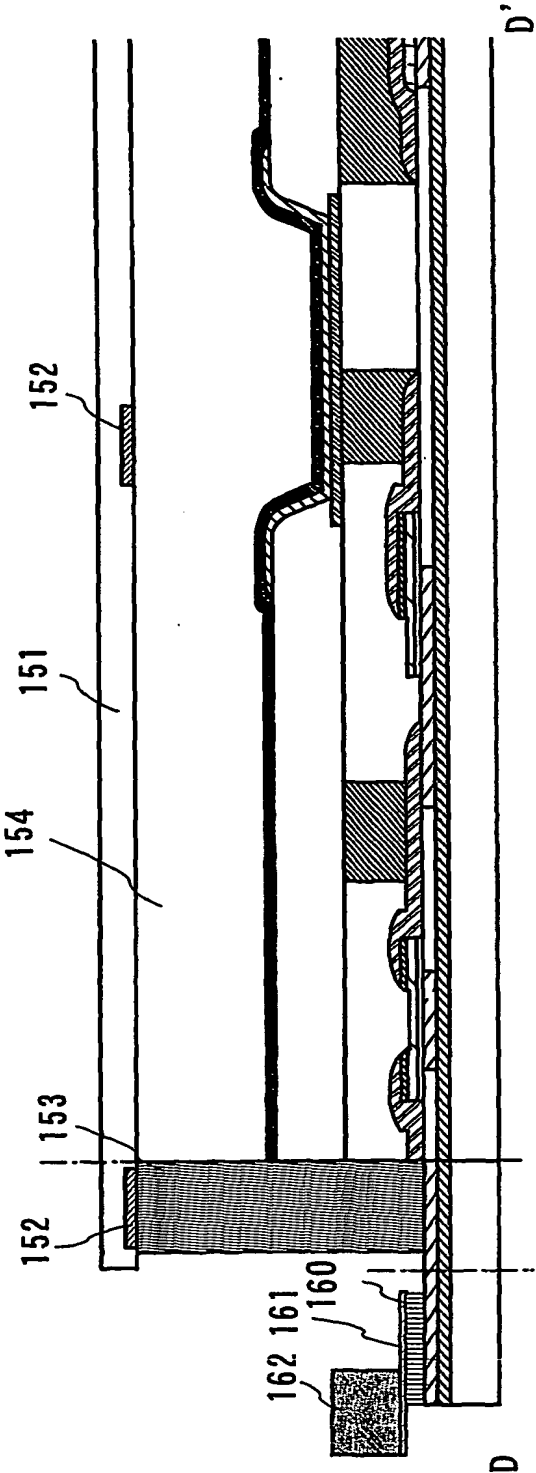
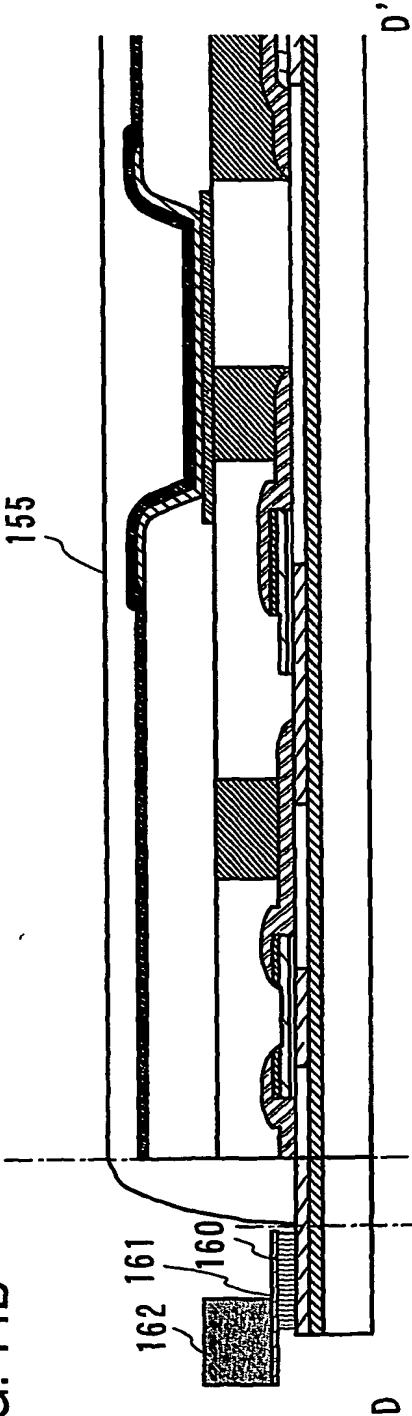


FIG. 14B



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FIG. 15A

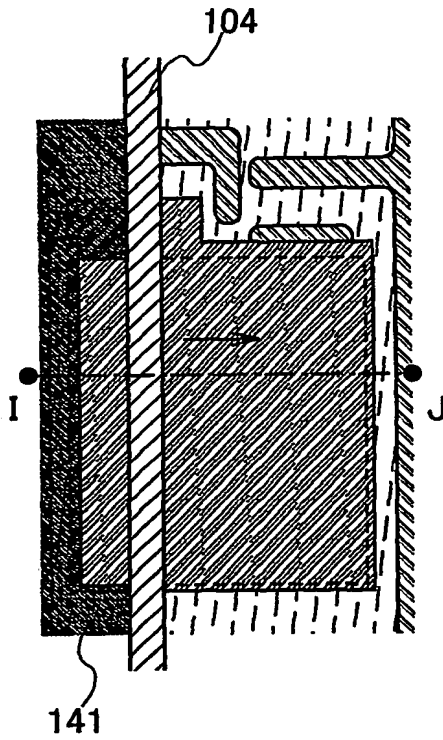


FIG. 15B

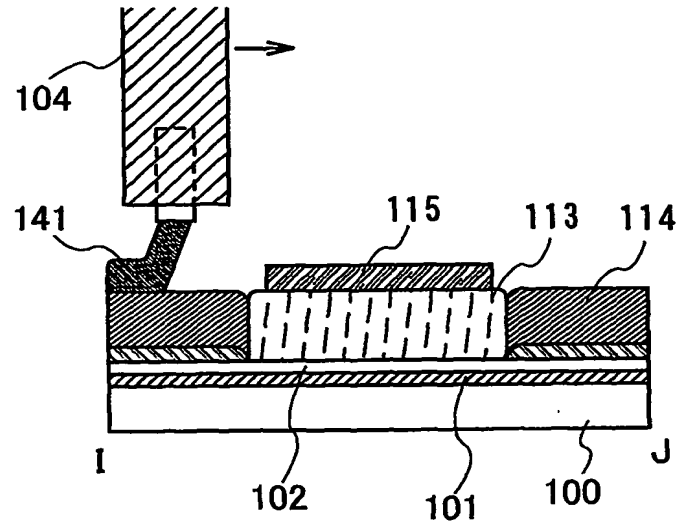


FIG. 15C

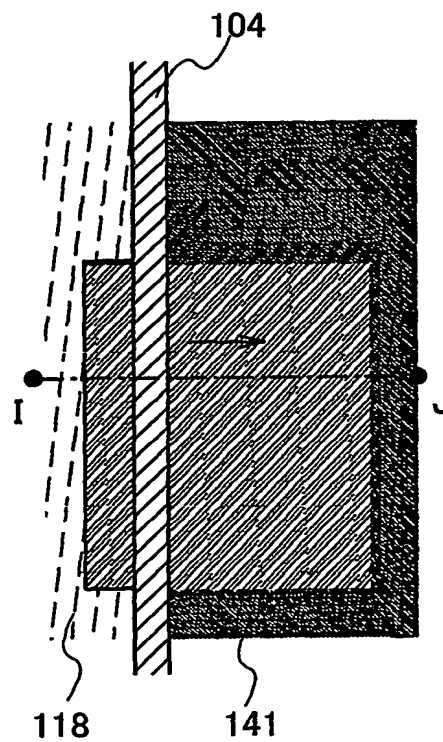
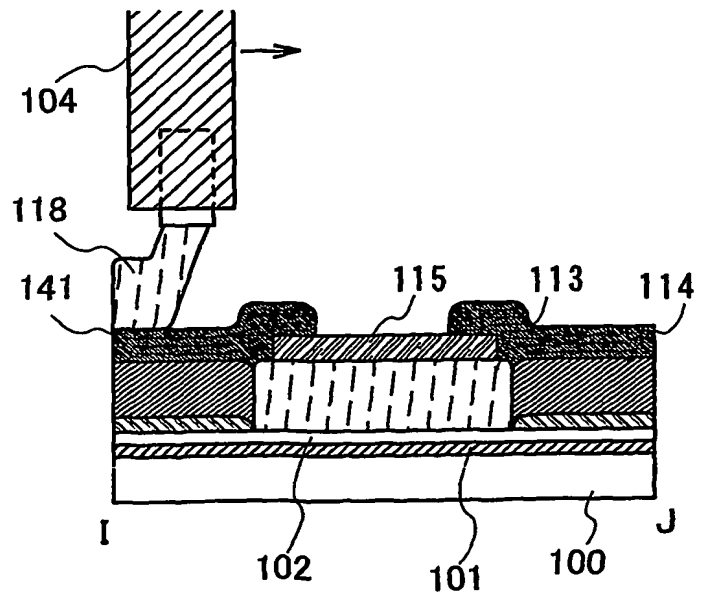


FIG. 15D



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FIG. 16A

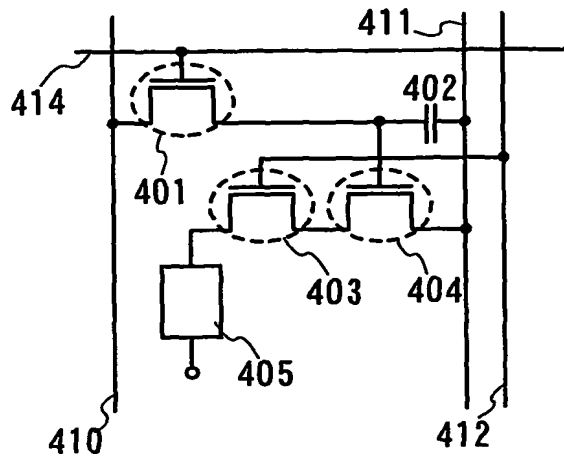


FIG. 16B

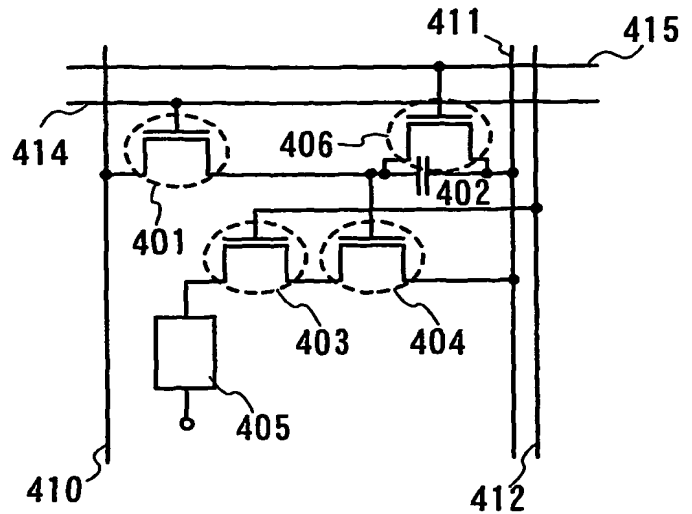


FIG. 16C

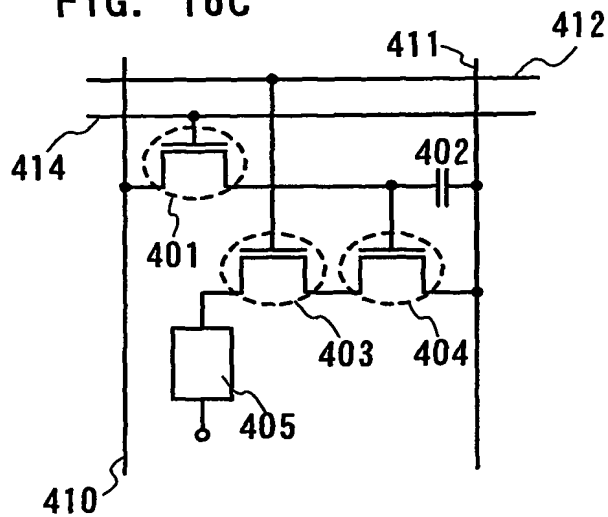


FIG. 16D

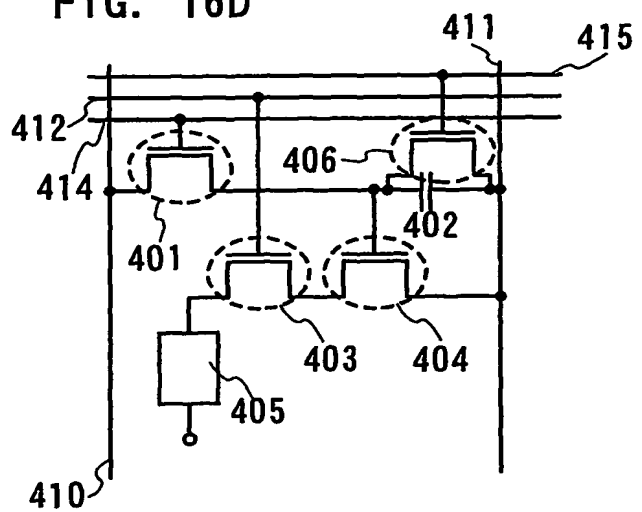


FIG. 16E

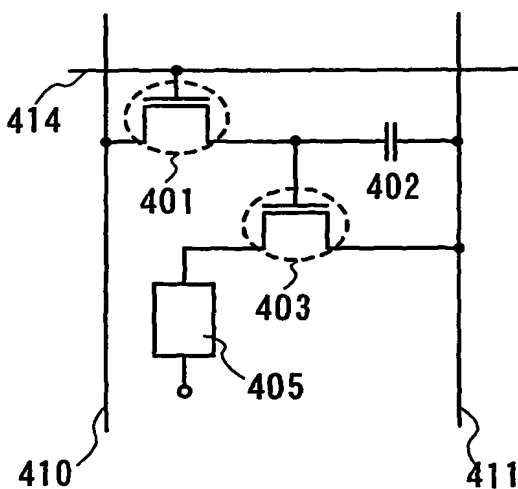
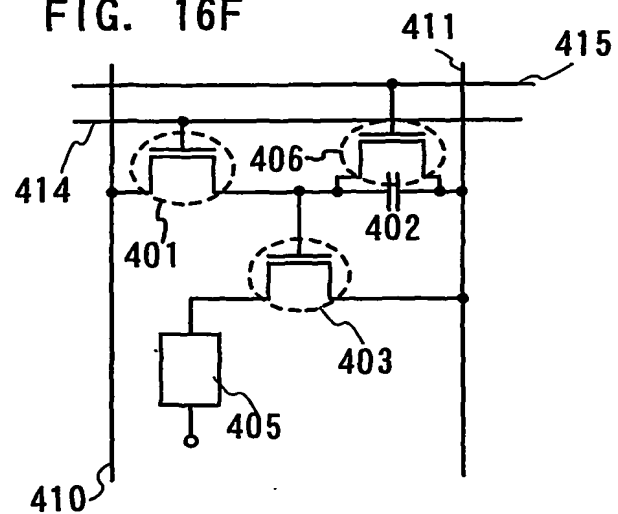


FIG. 16F



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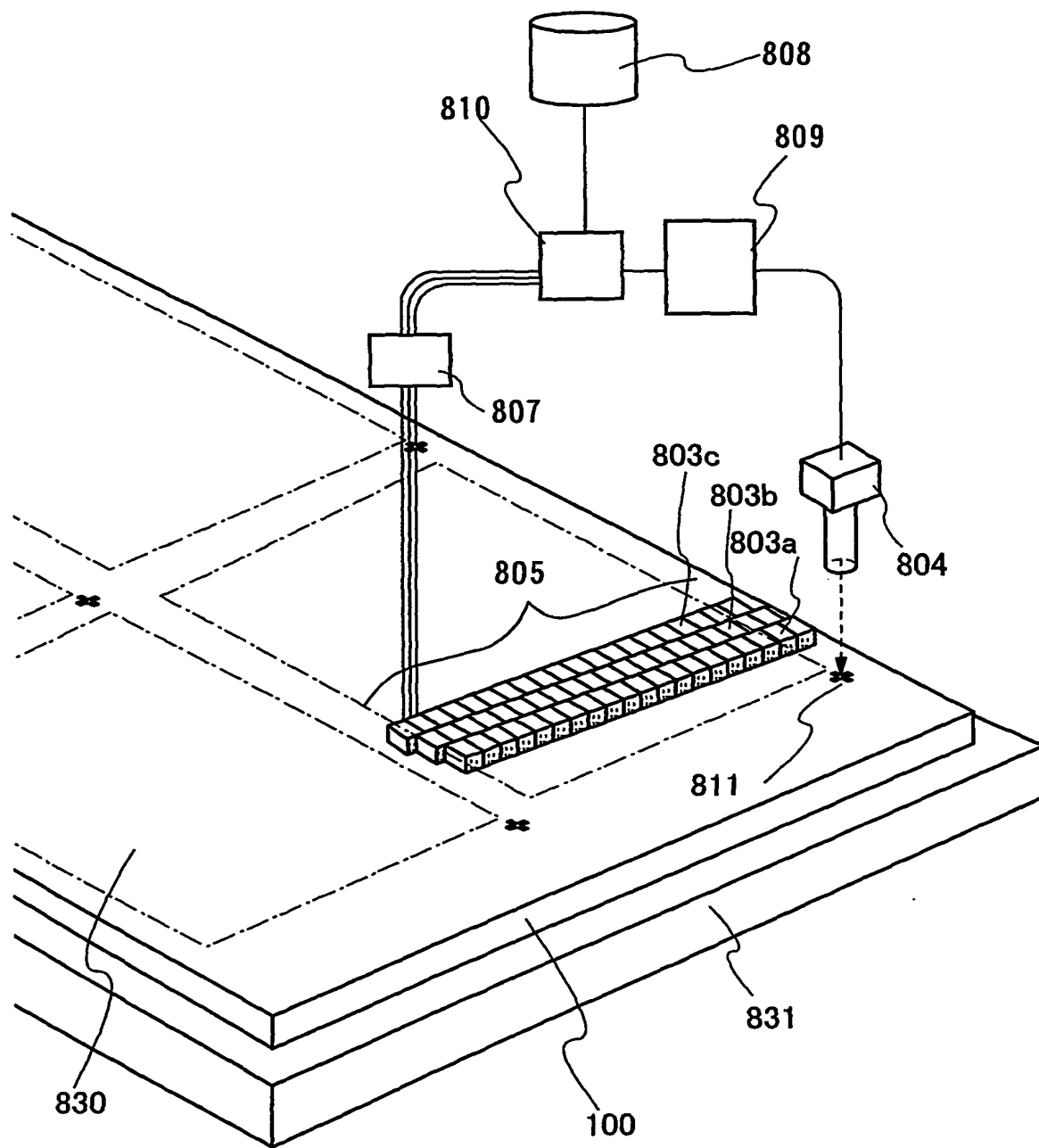


FIG. 17

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FIG. 18A

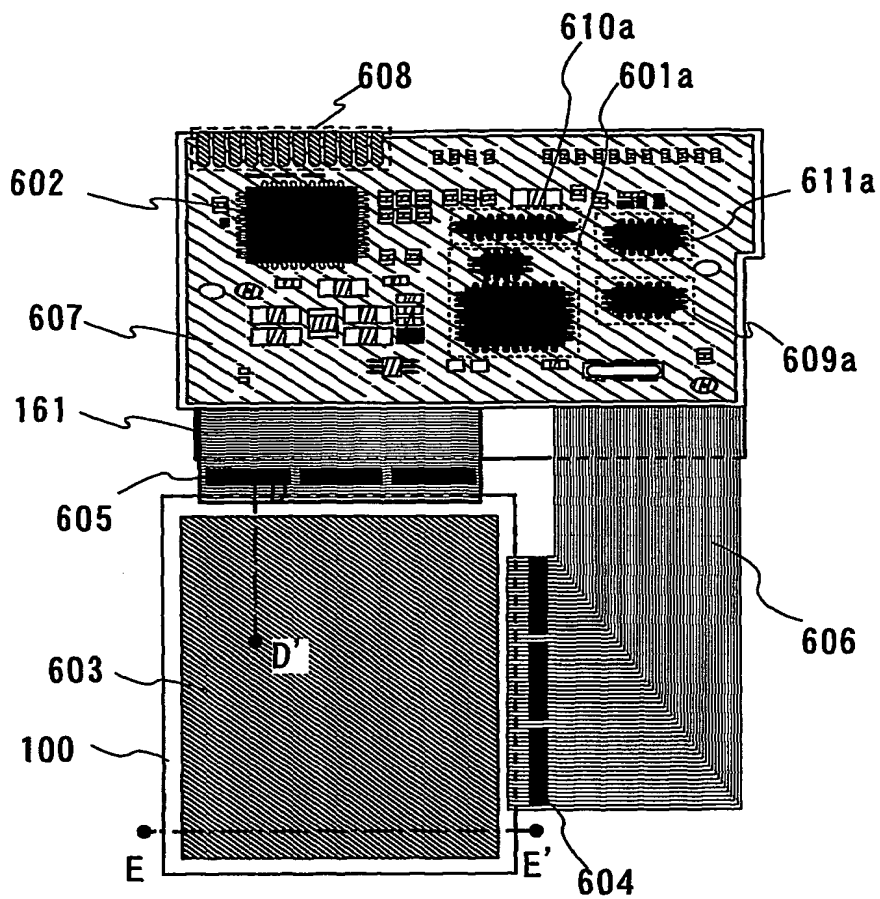
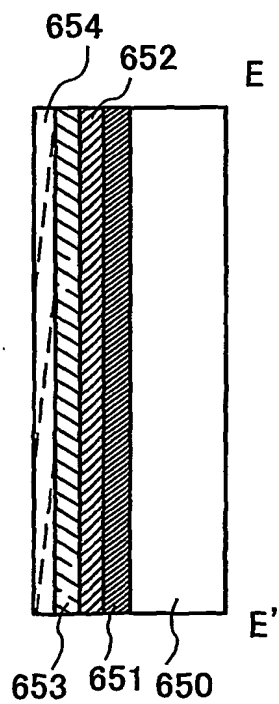


FIG. 18B



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FIG. 19A

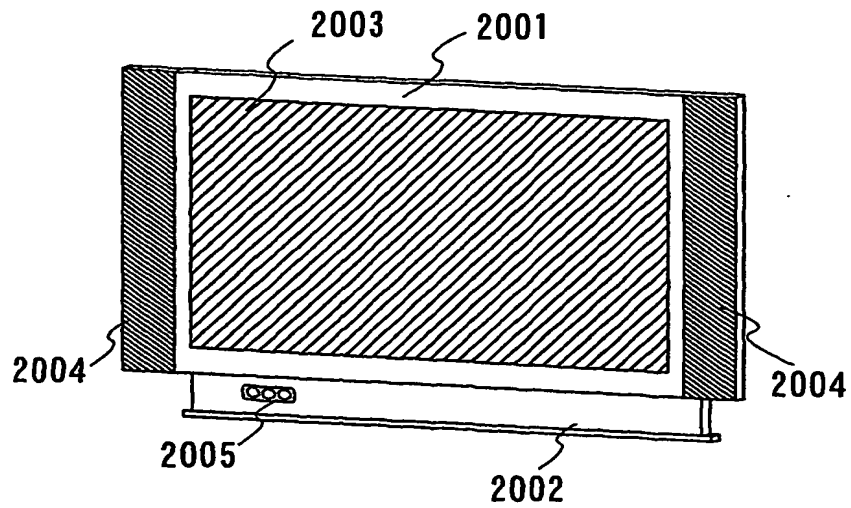
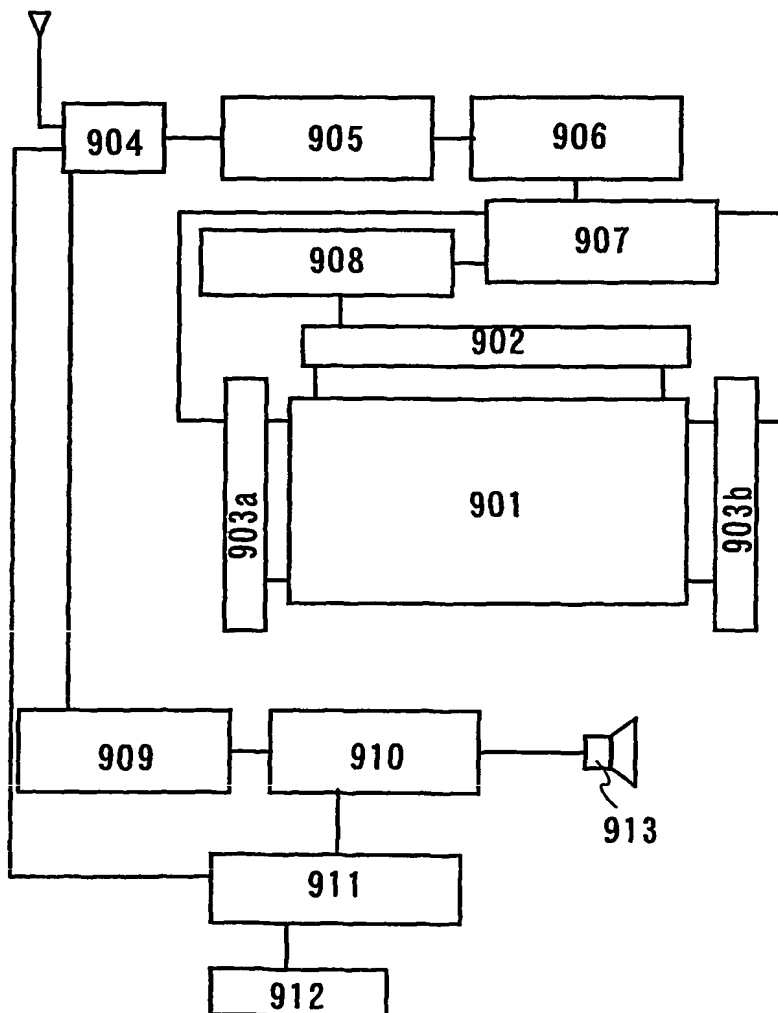


FIG. 19B



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FIG. 20A

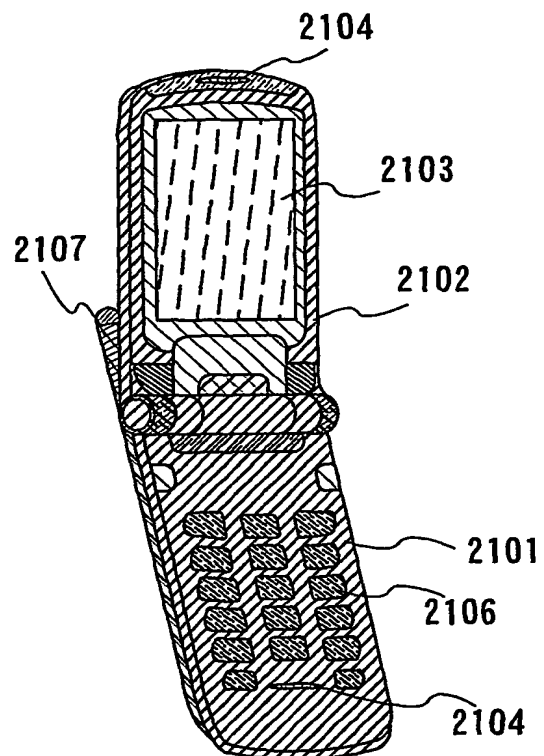


FIG. 20B

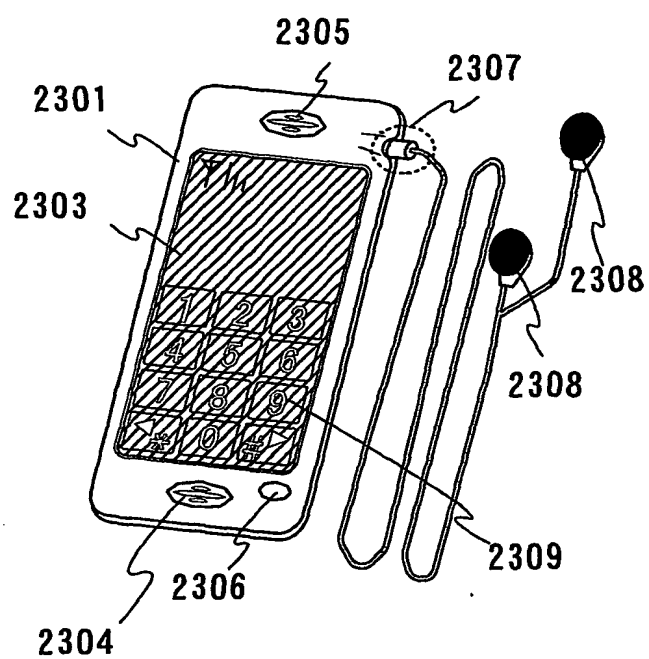
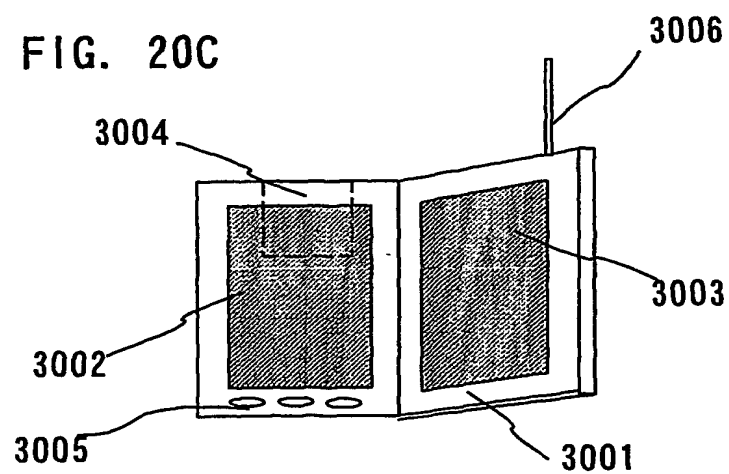


FIG. 20C



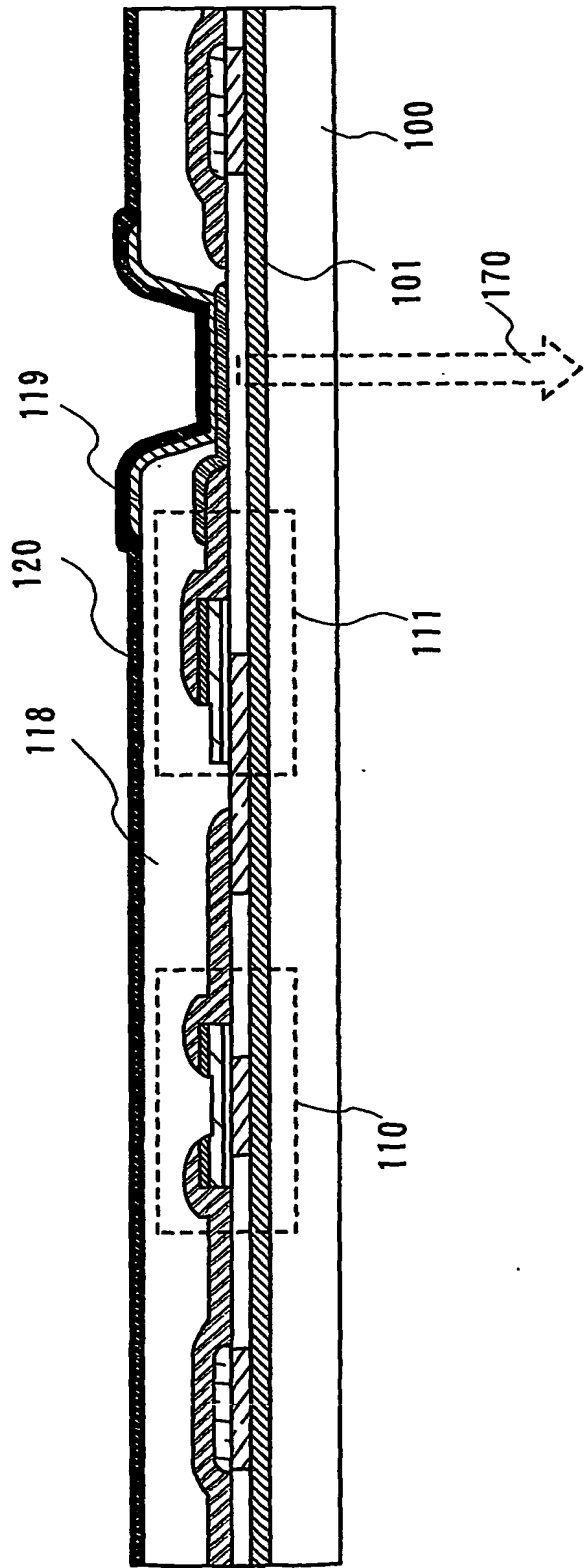


FIG. 21

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FIG. 22A

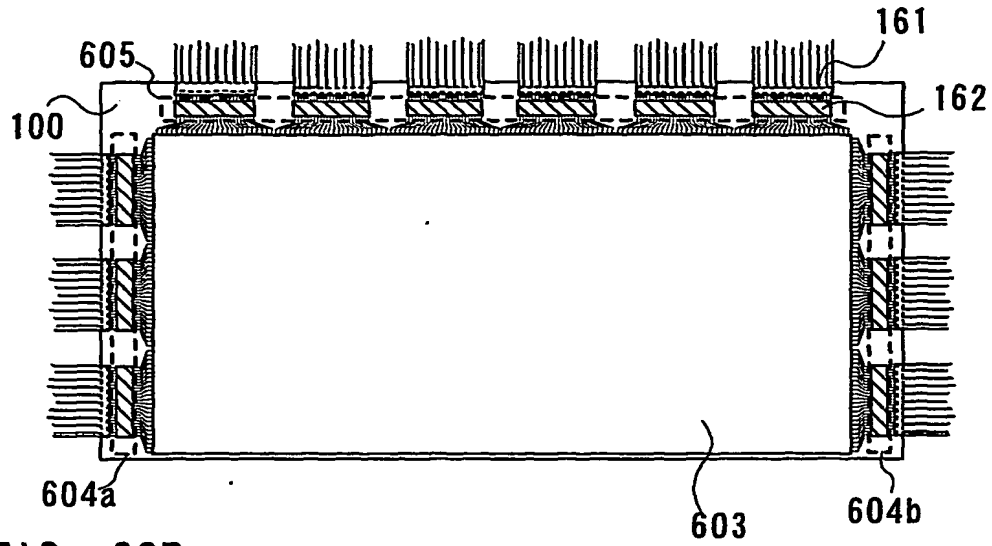


FIG. 22B

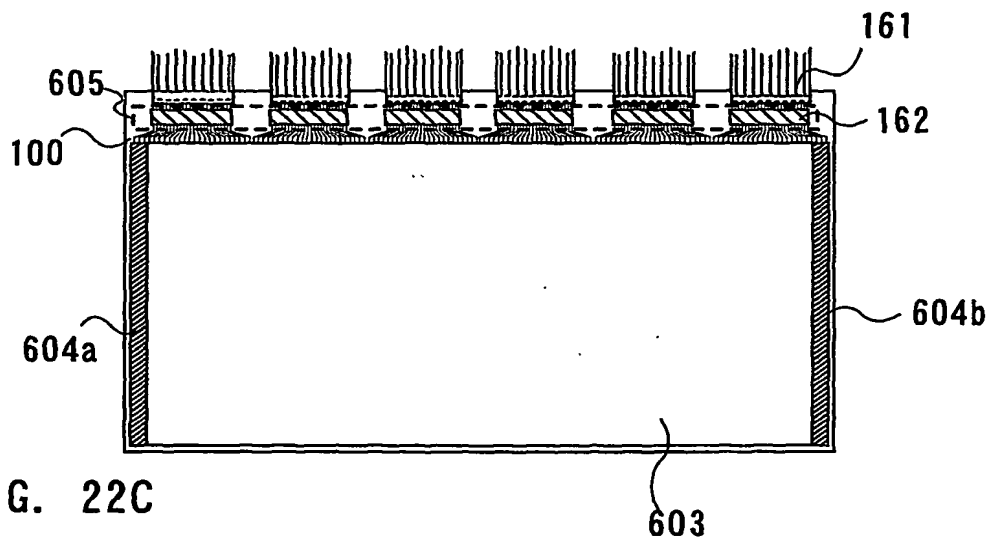
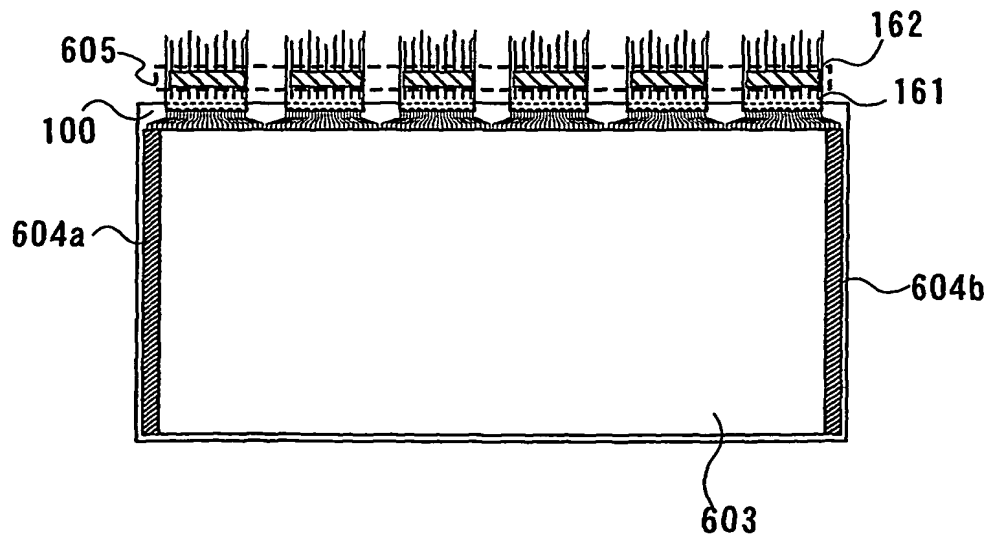


FIG. 22C



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FIG. 23A

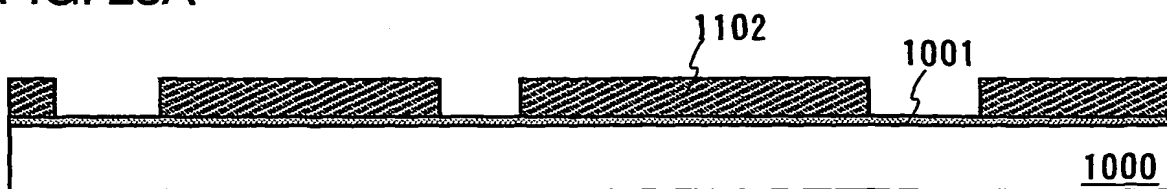


FIG. 23B

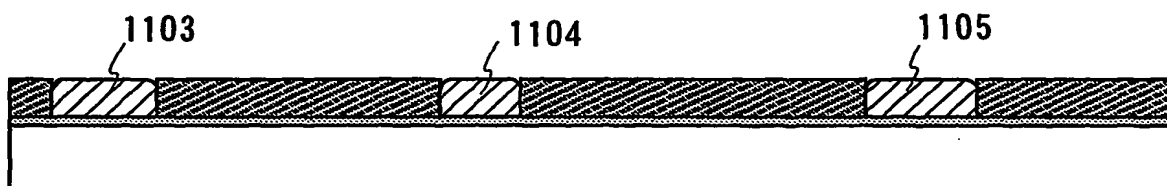


FIG. 23C

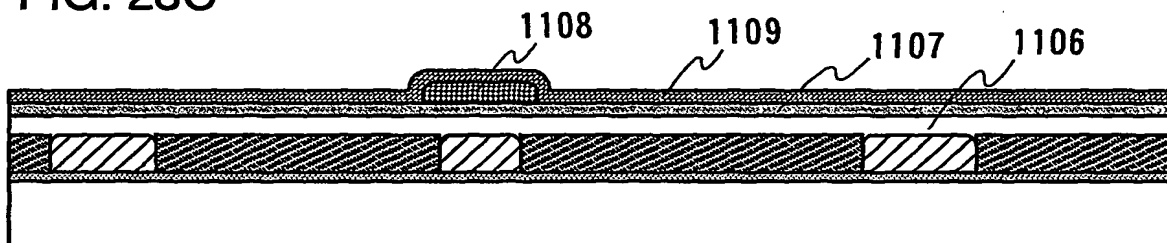


FIG. 23D

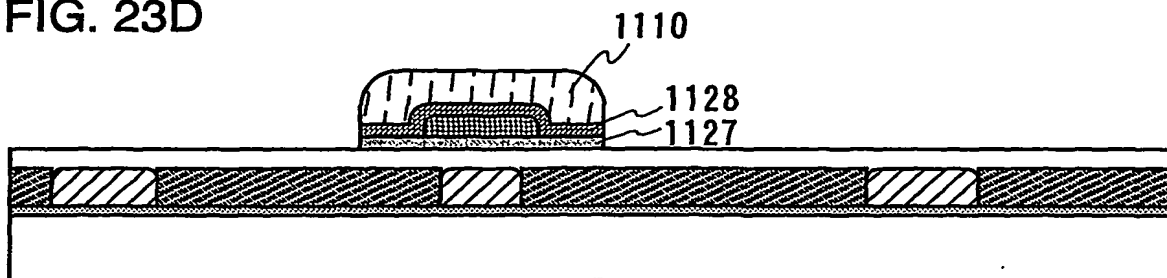


FIG. 23E

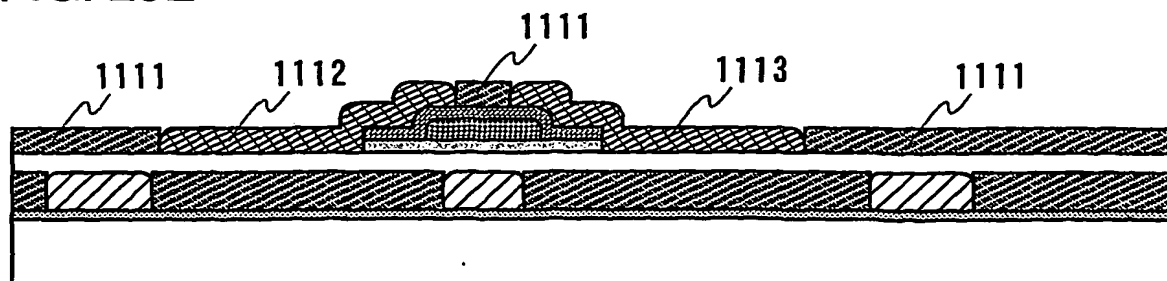


FIG. 24A

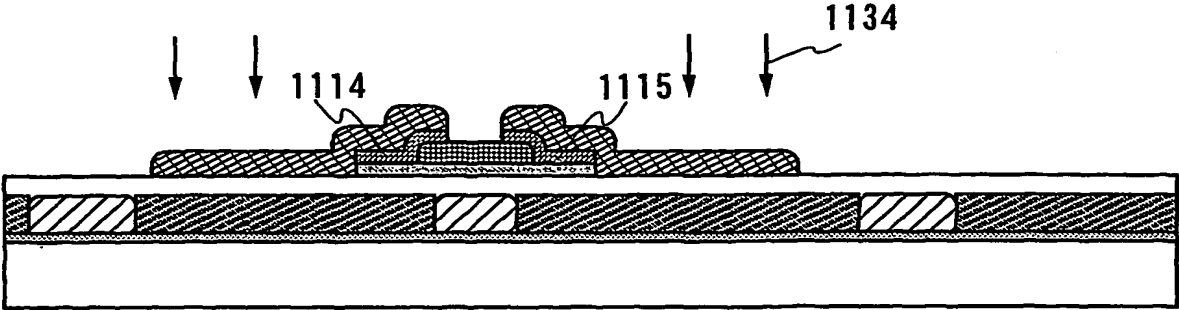


FIG. 24B

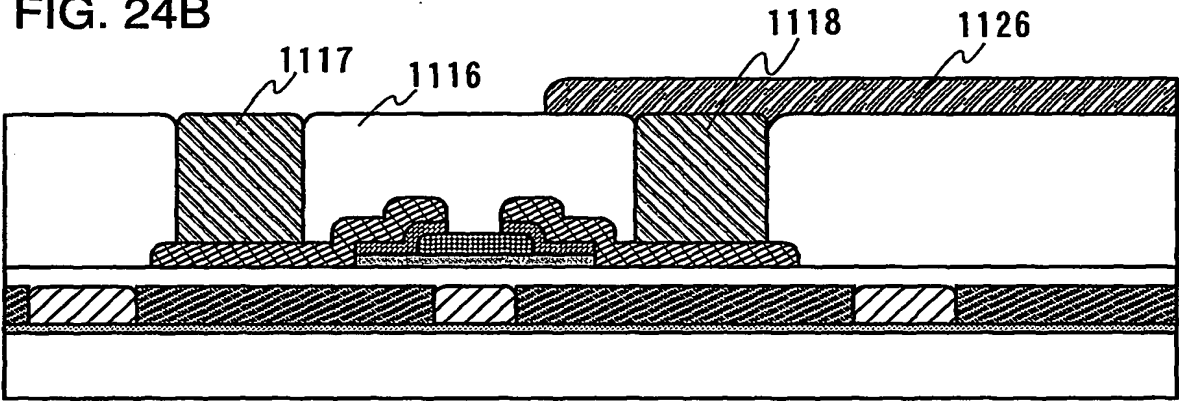


FIG. 24C

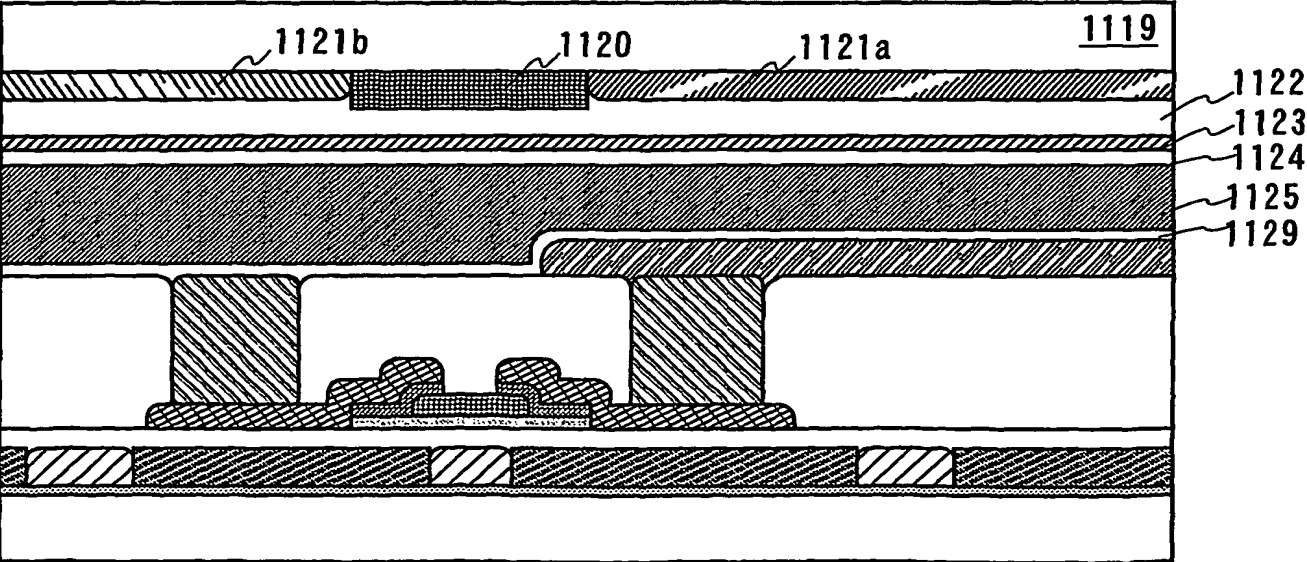


FIG. 25A

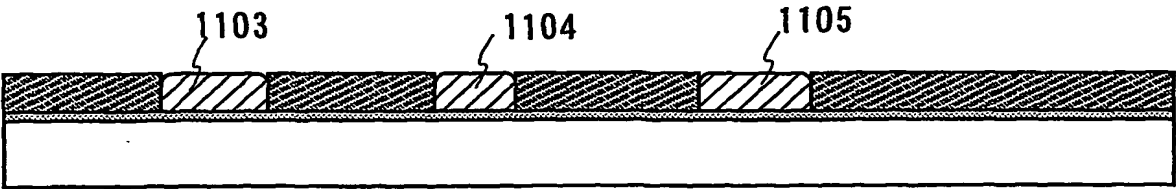


FIG. 25B

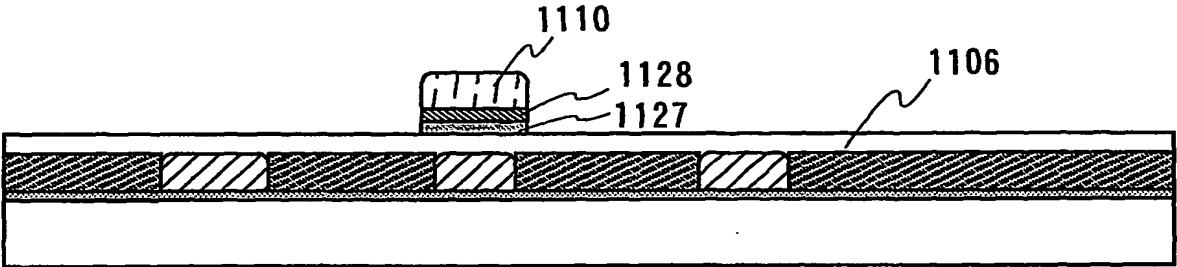


FIG. 25C

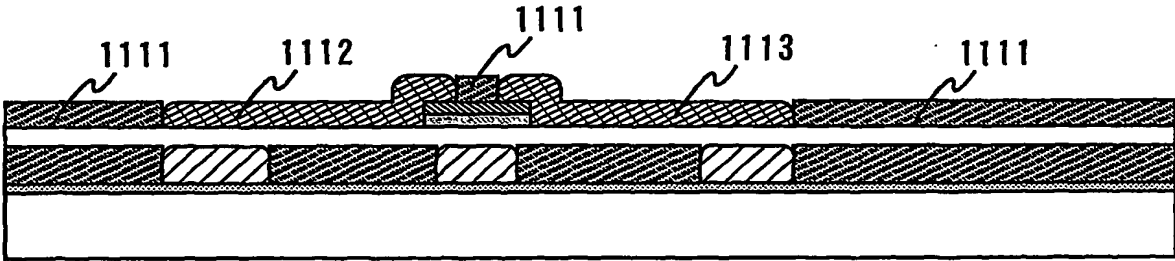
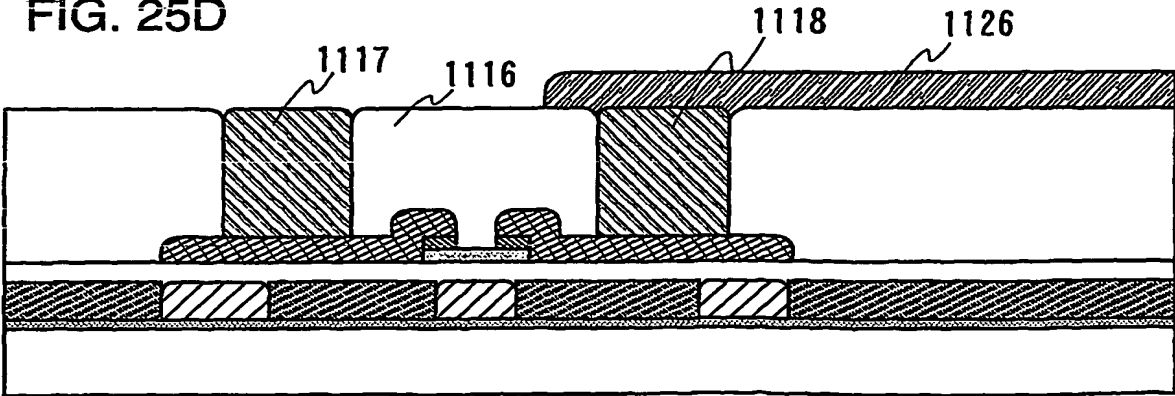


FIG. 25D



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FIG. 26A

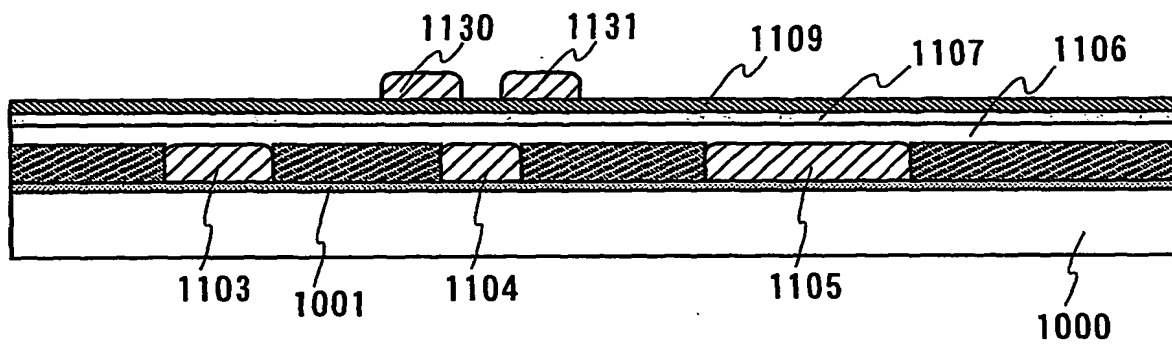


FIG. 26B

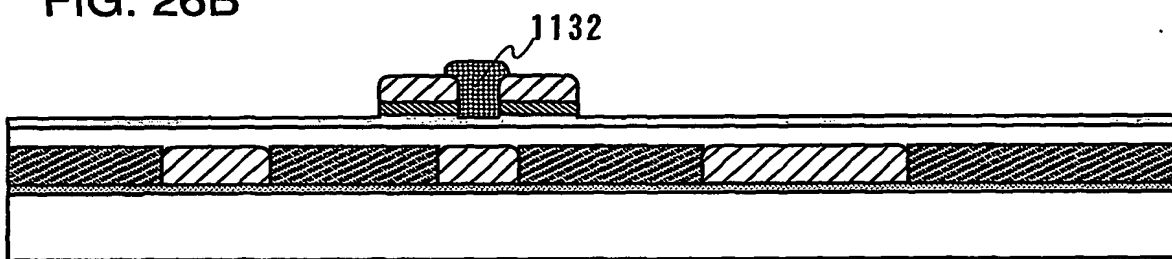


FIG. 26C

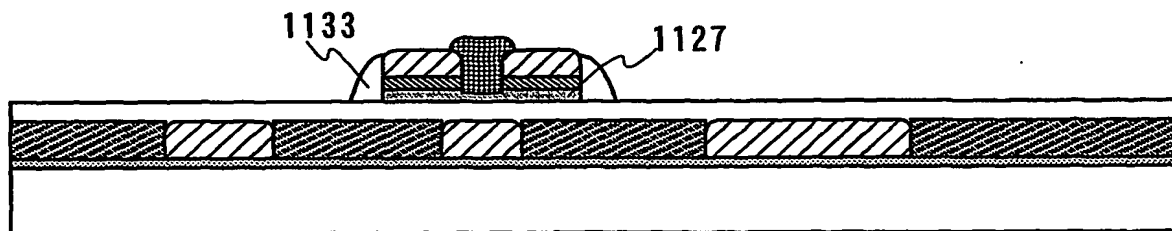
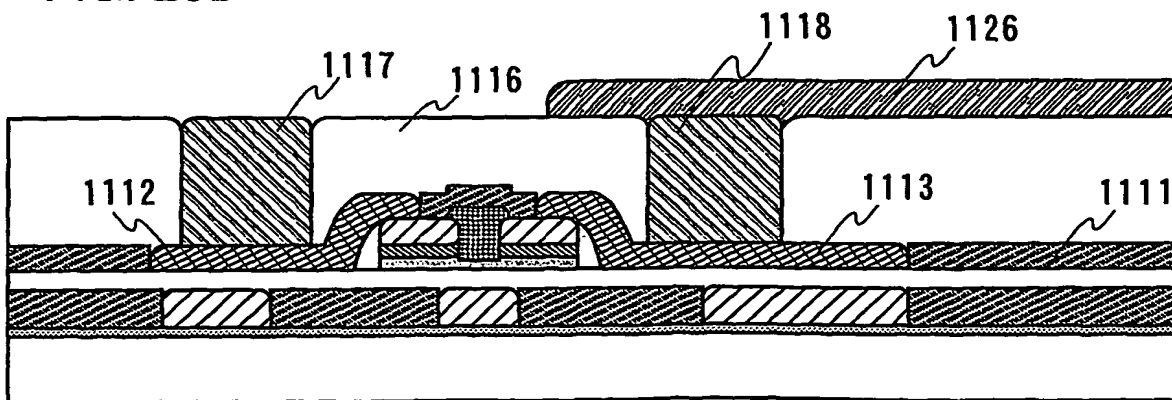


FIG. 26D



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FIG. 27A

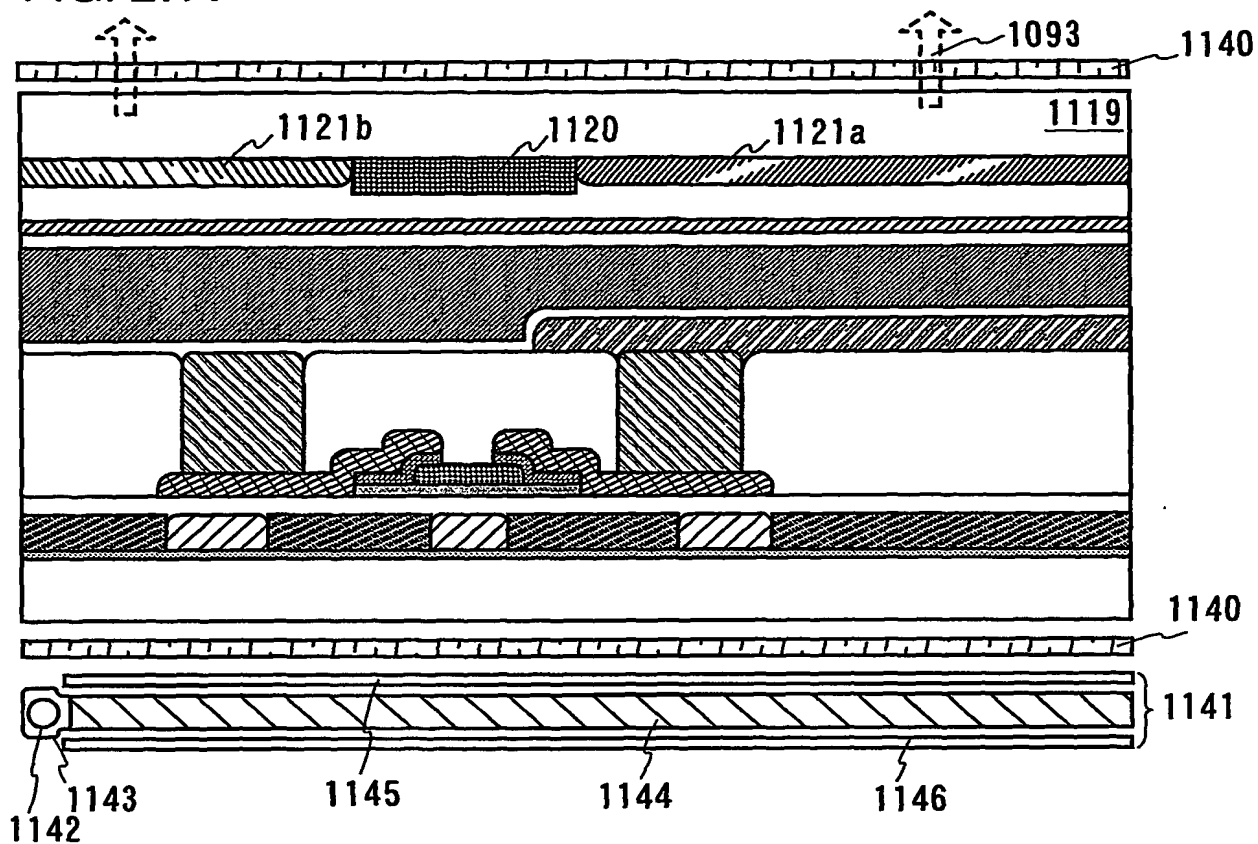
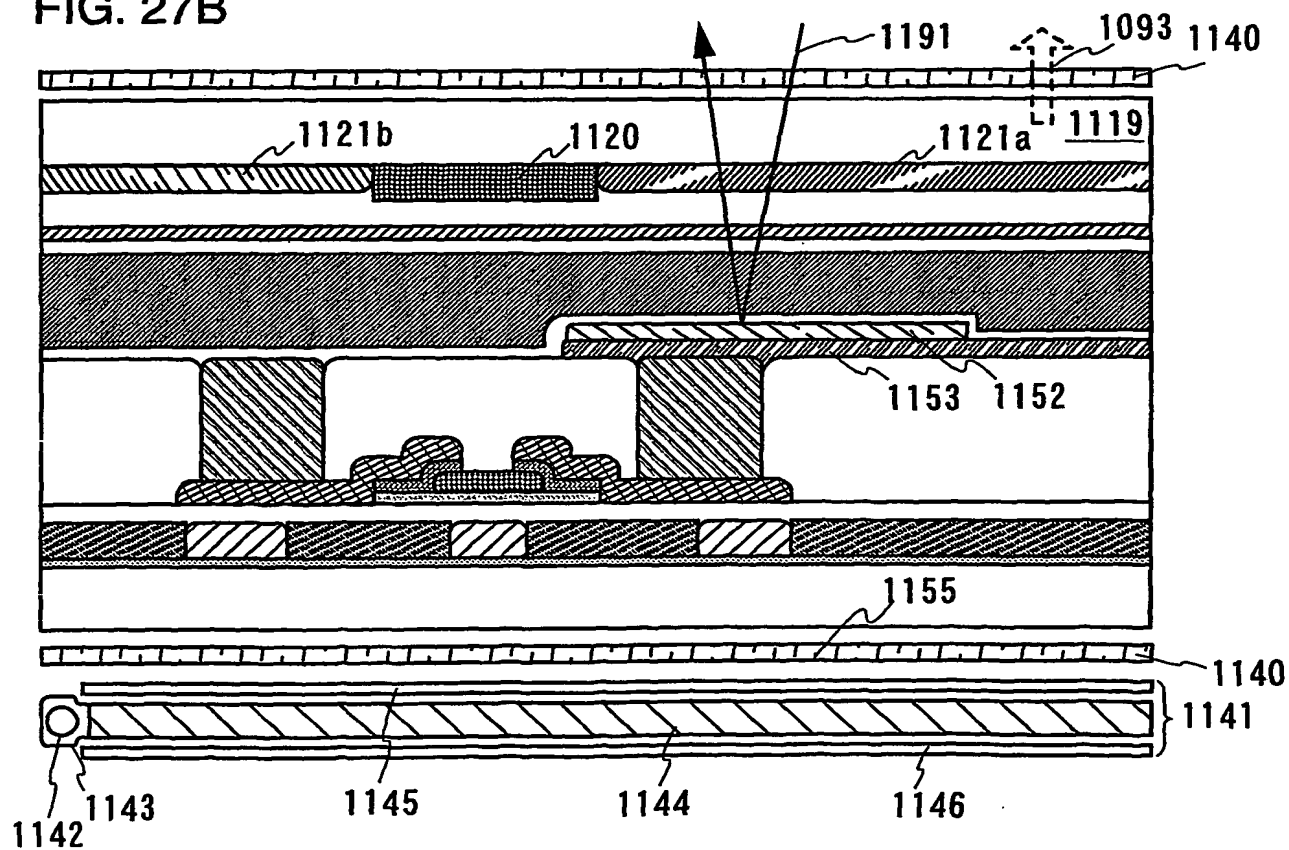


FIG. 27B



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FIG. 28A

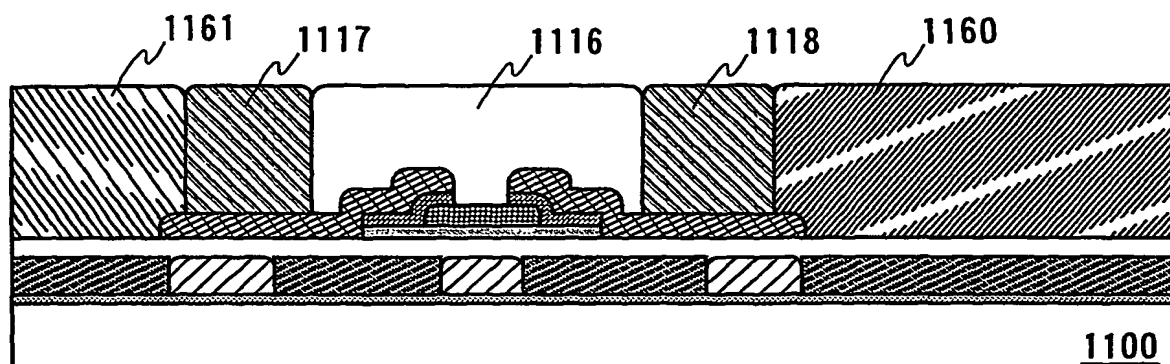
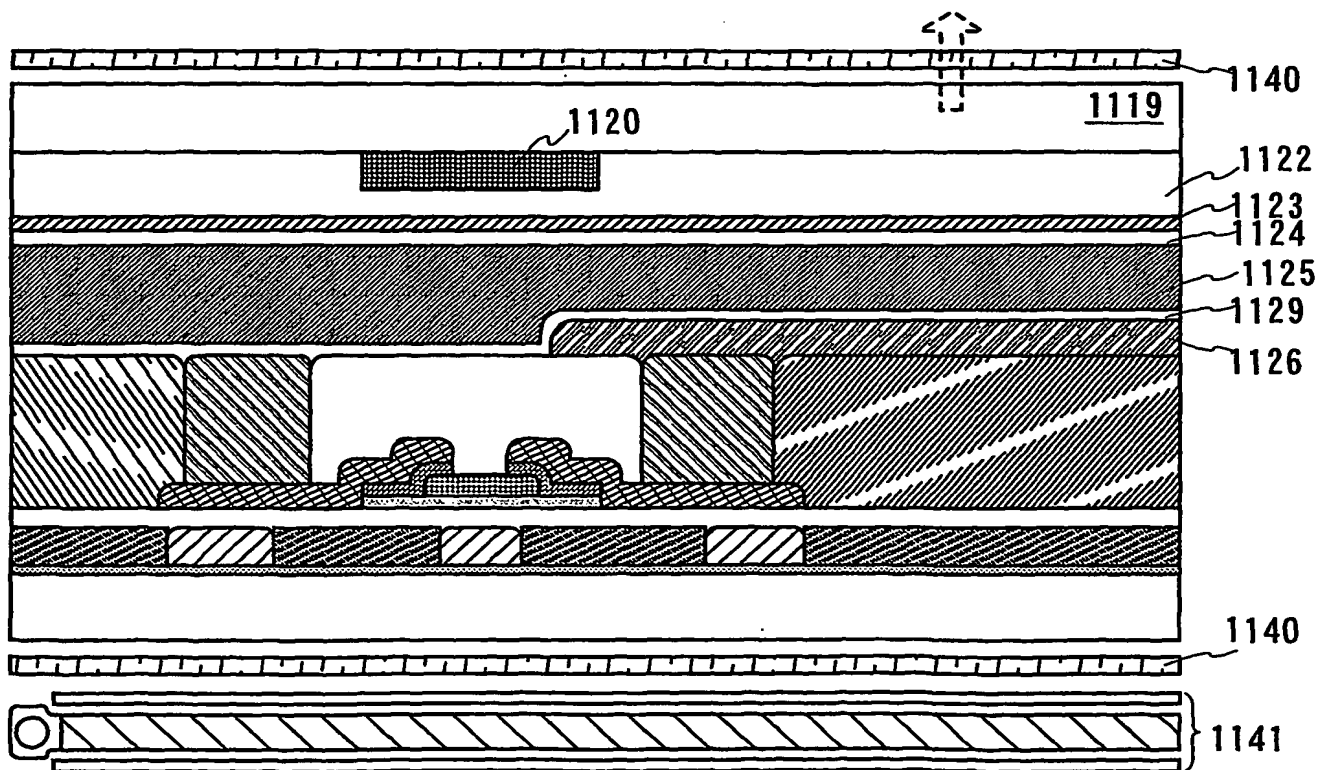


FIG. 28B



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FIG. 29A

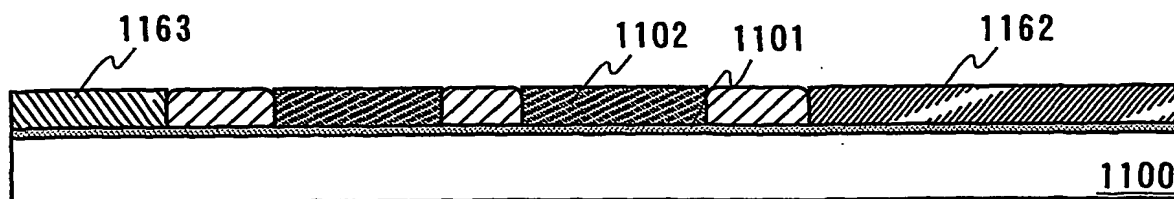


FIG. 29B

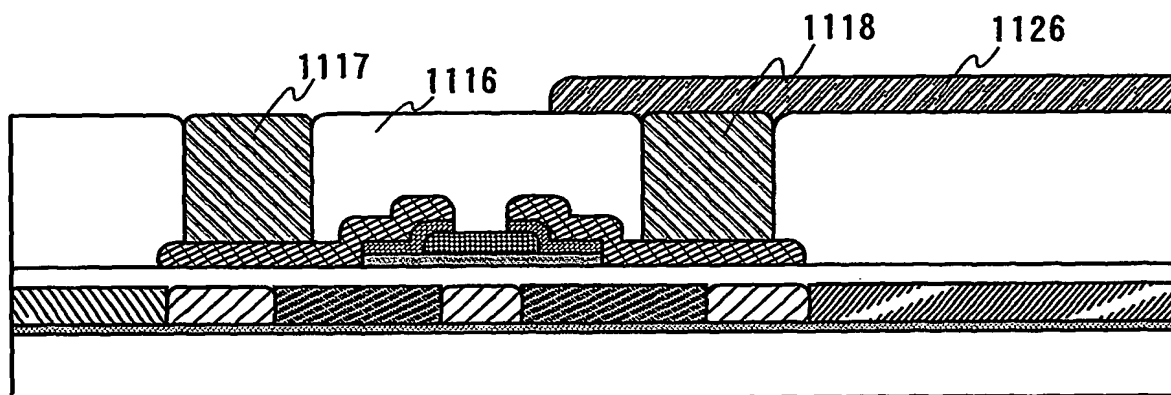


FIG. 29C

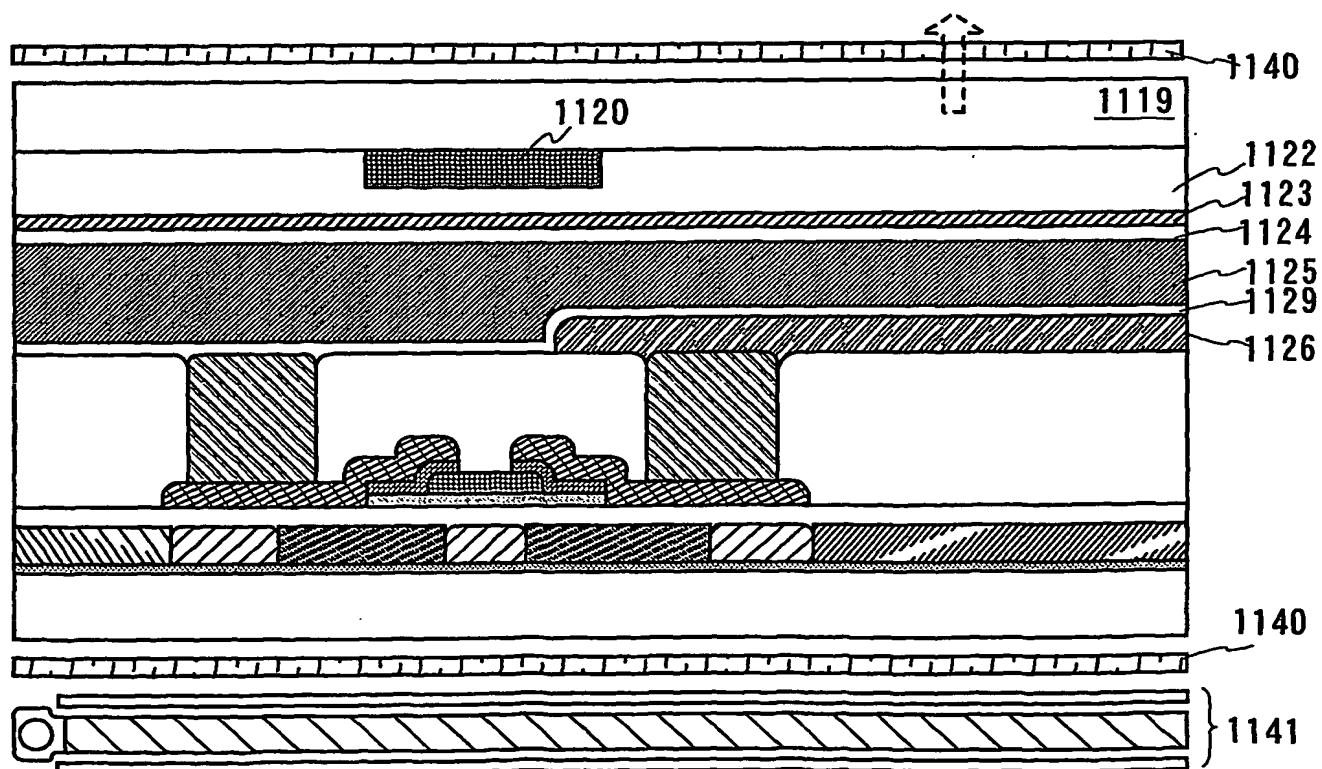


FIG. 30A

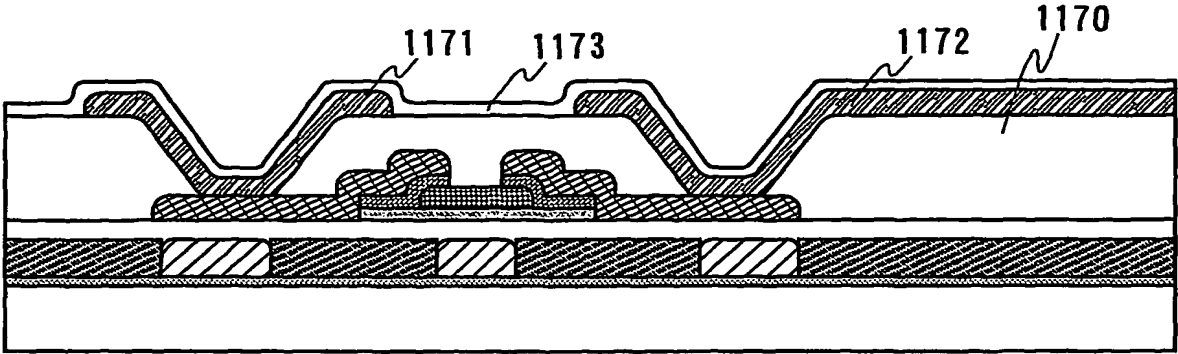


FIG. 30B

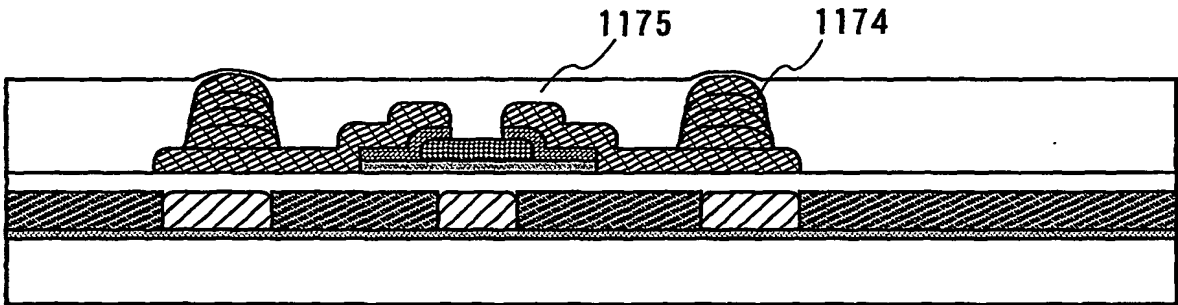


FIG. 30C

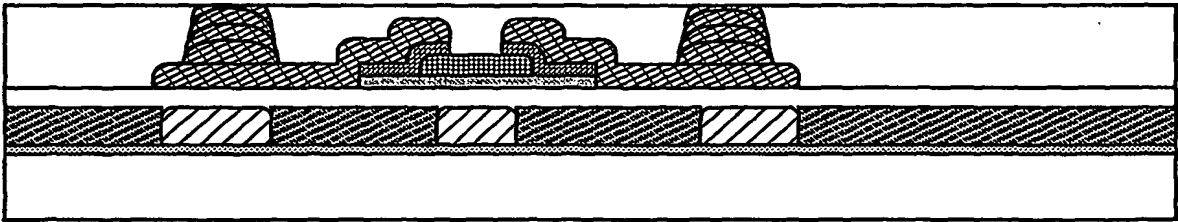
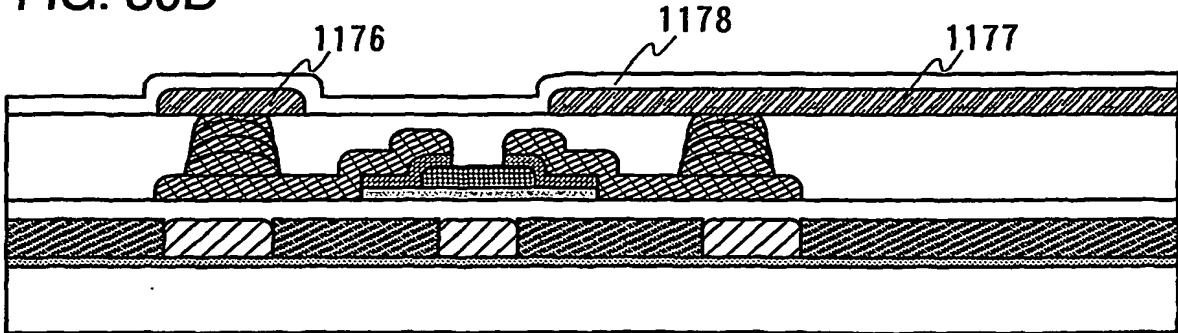


FIG. 30D



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FIG. 31A

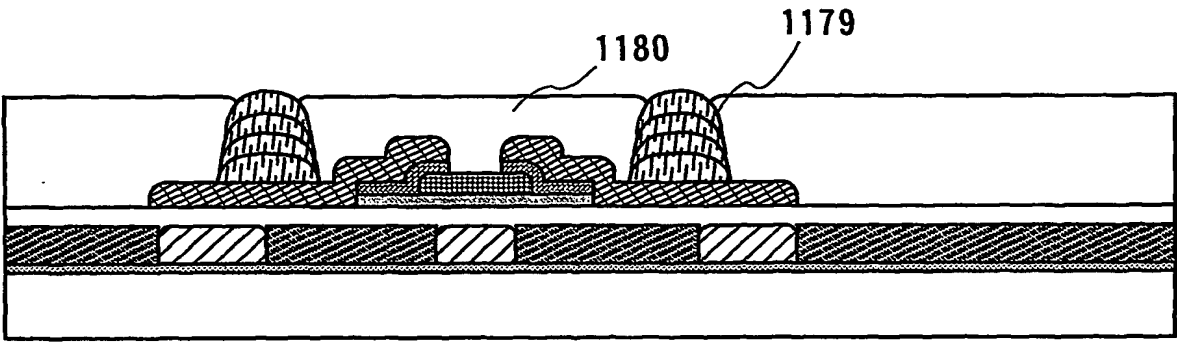


FIG. 31B

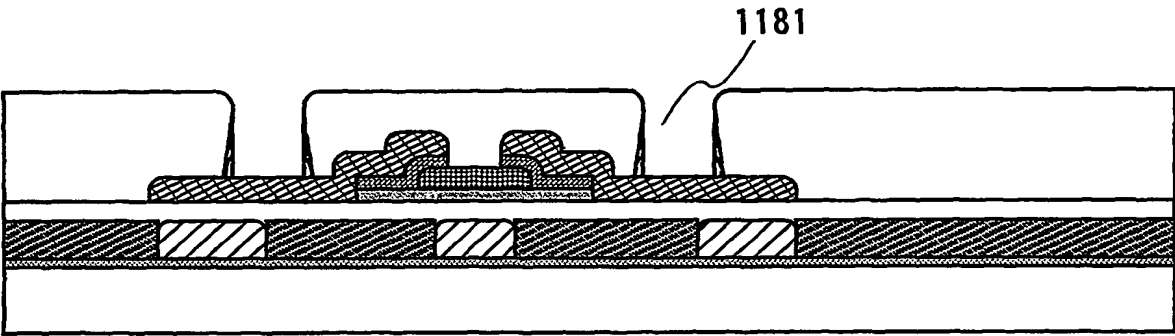
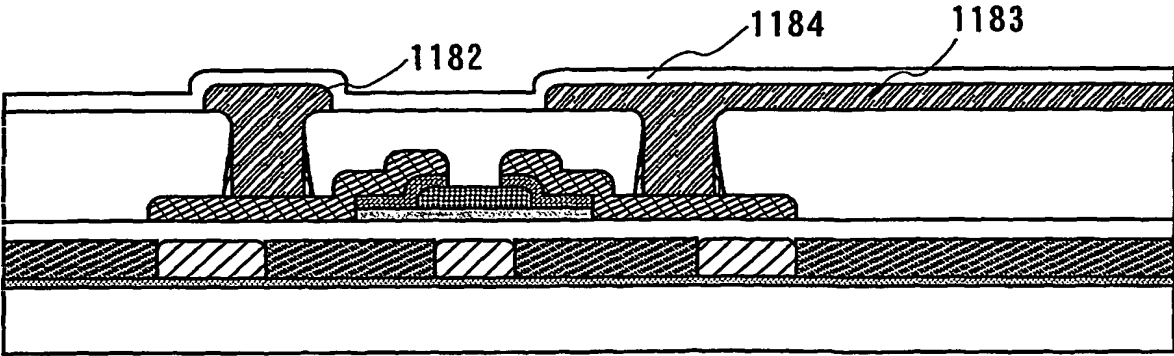


FIG. 31C



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FIG. 32A

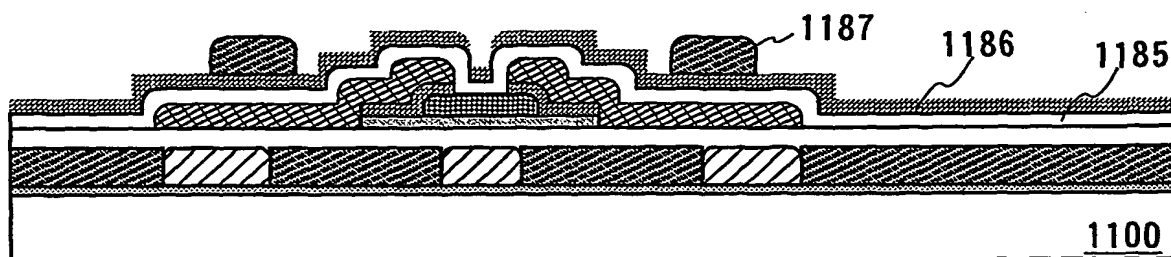


FIG. 32B

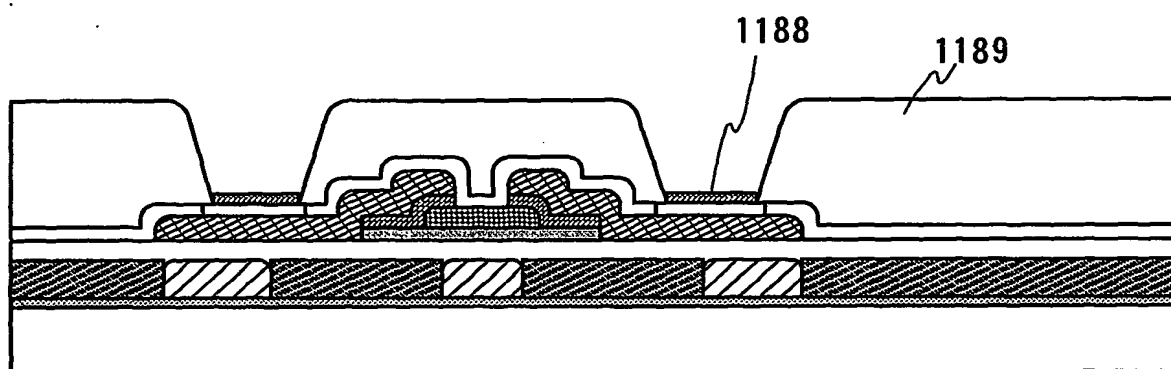


FIG. 32C

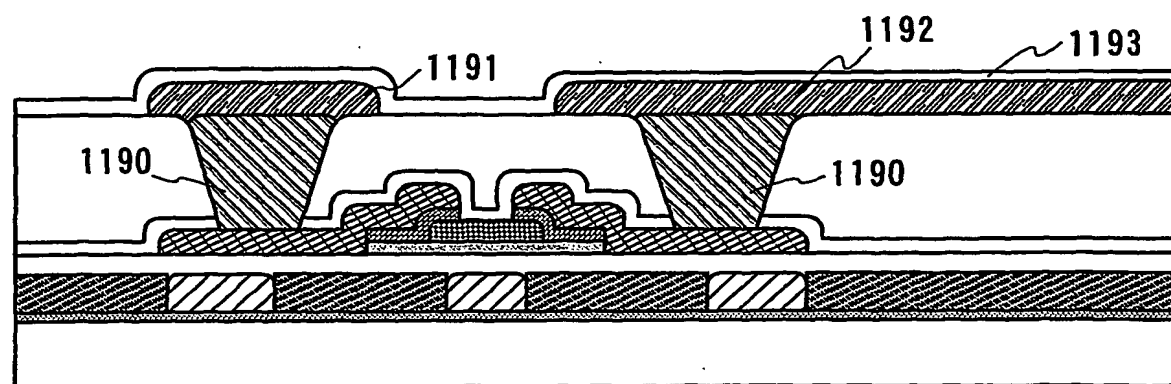


FIG. 33A

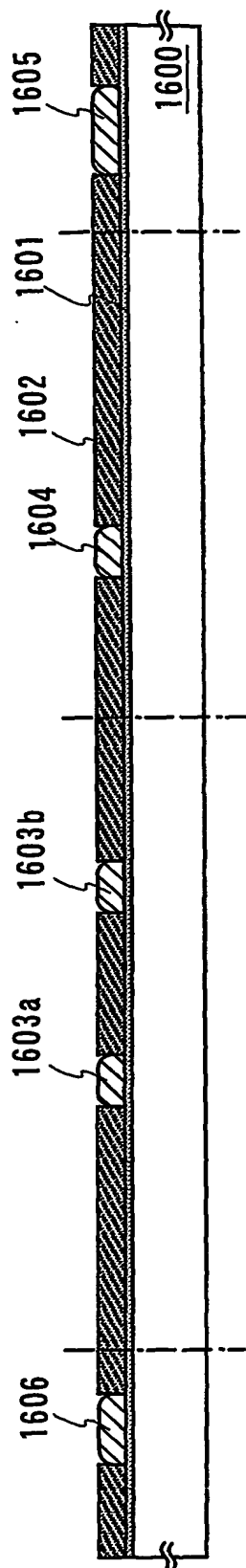


FIG. 33B

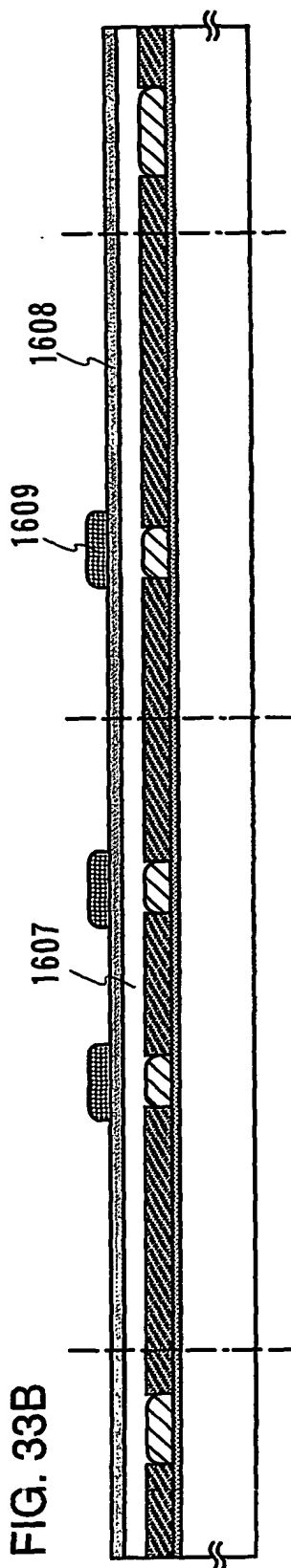


FIG. 33C

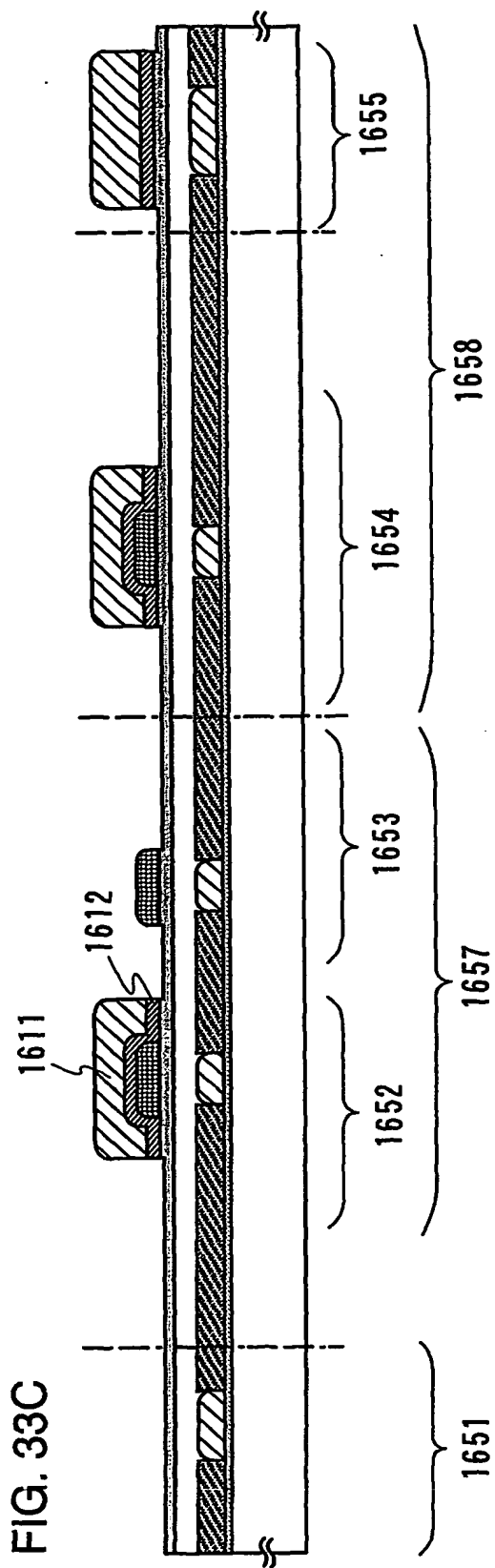


FIG. 34A

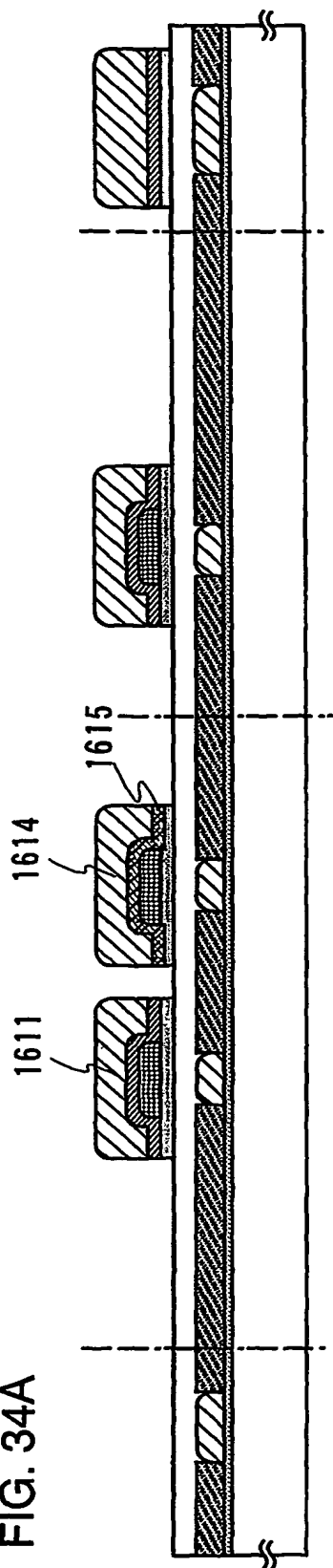


FIG. 34B

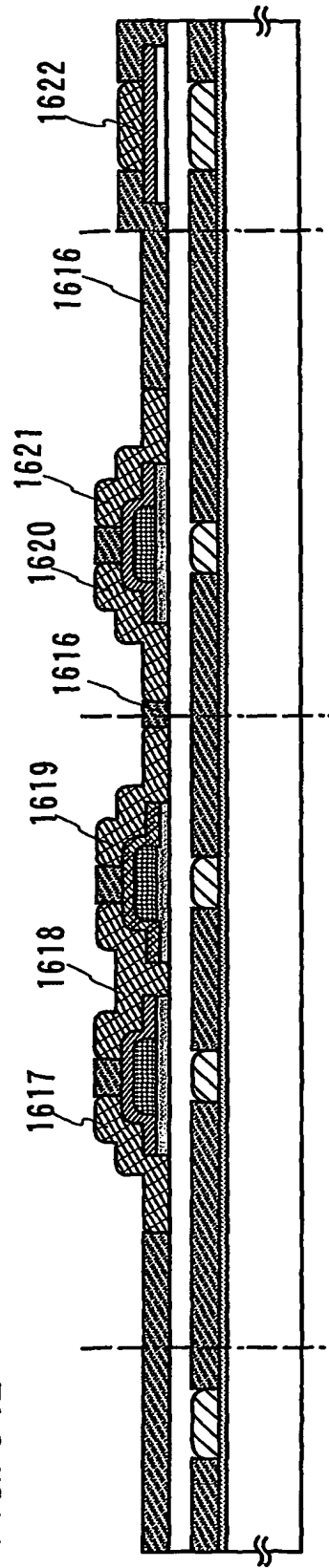


FIG. 34C

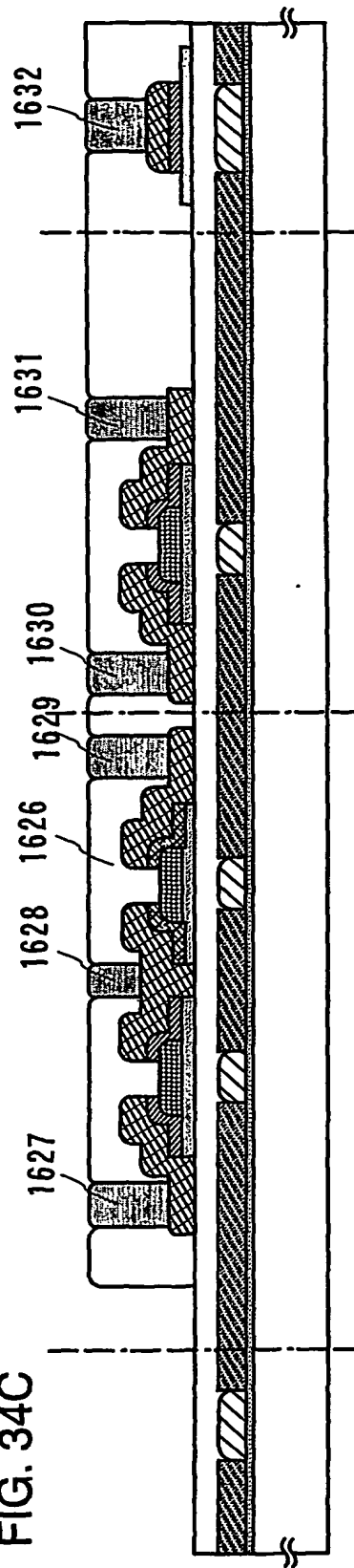


FIG. 35A

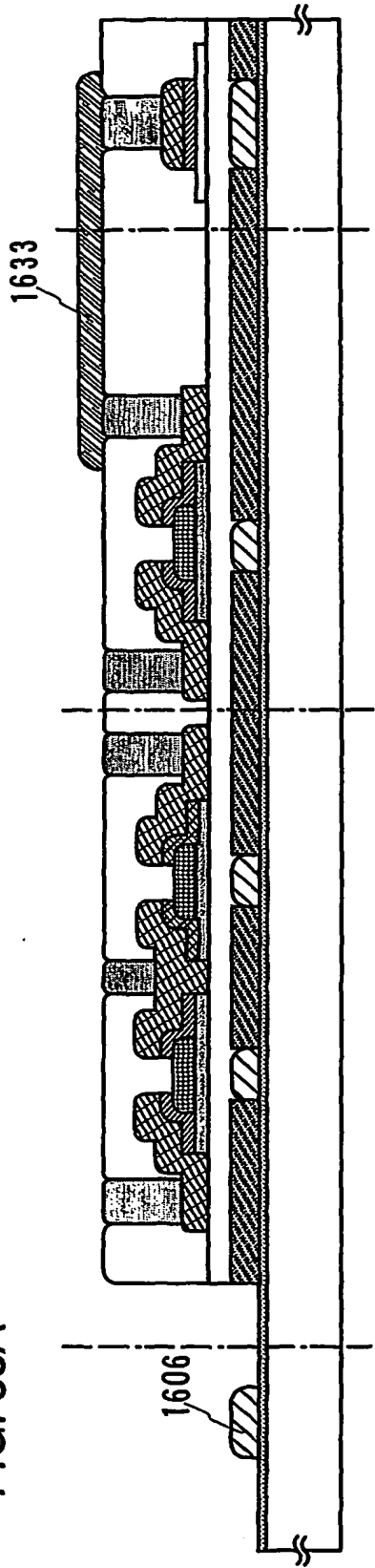
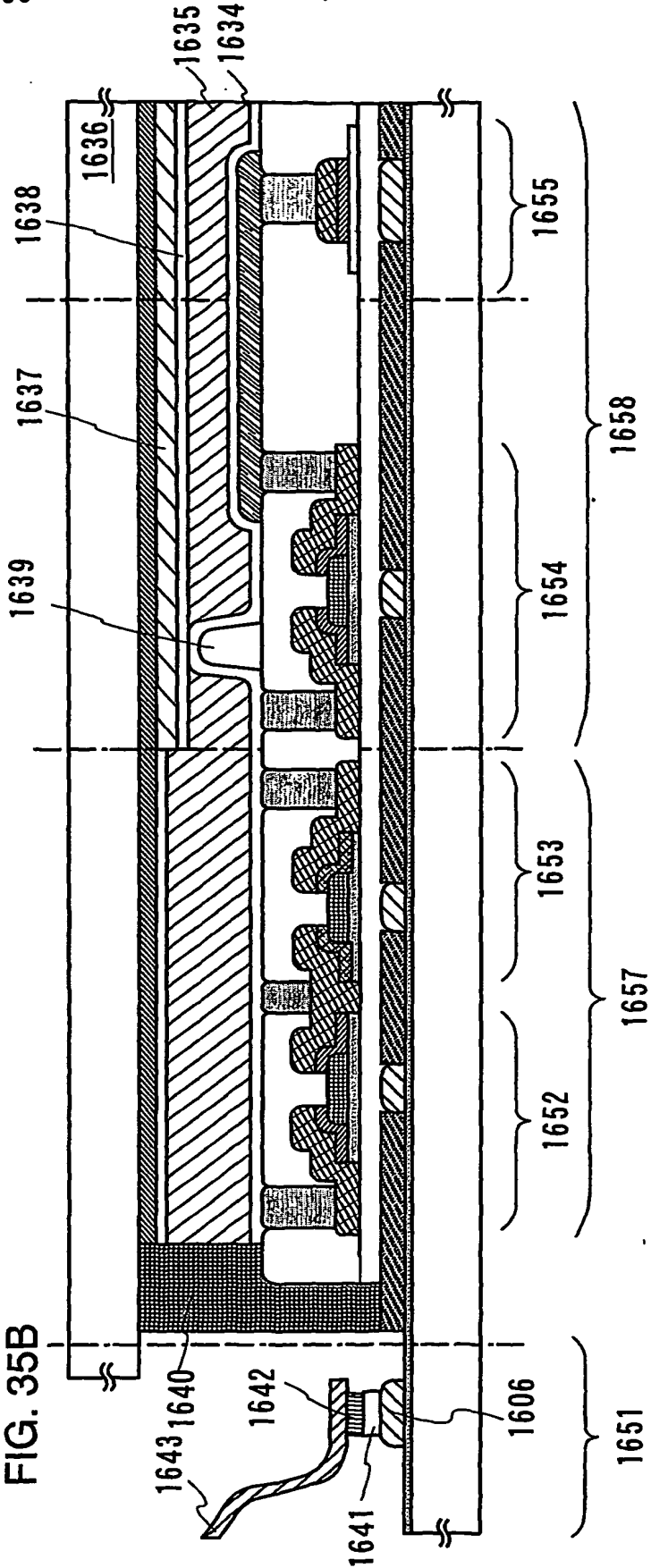
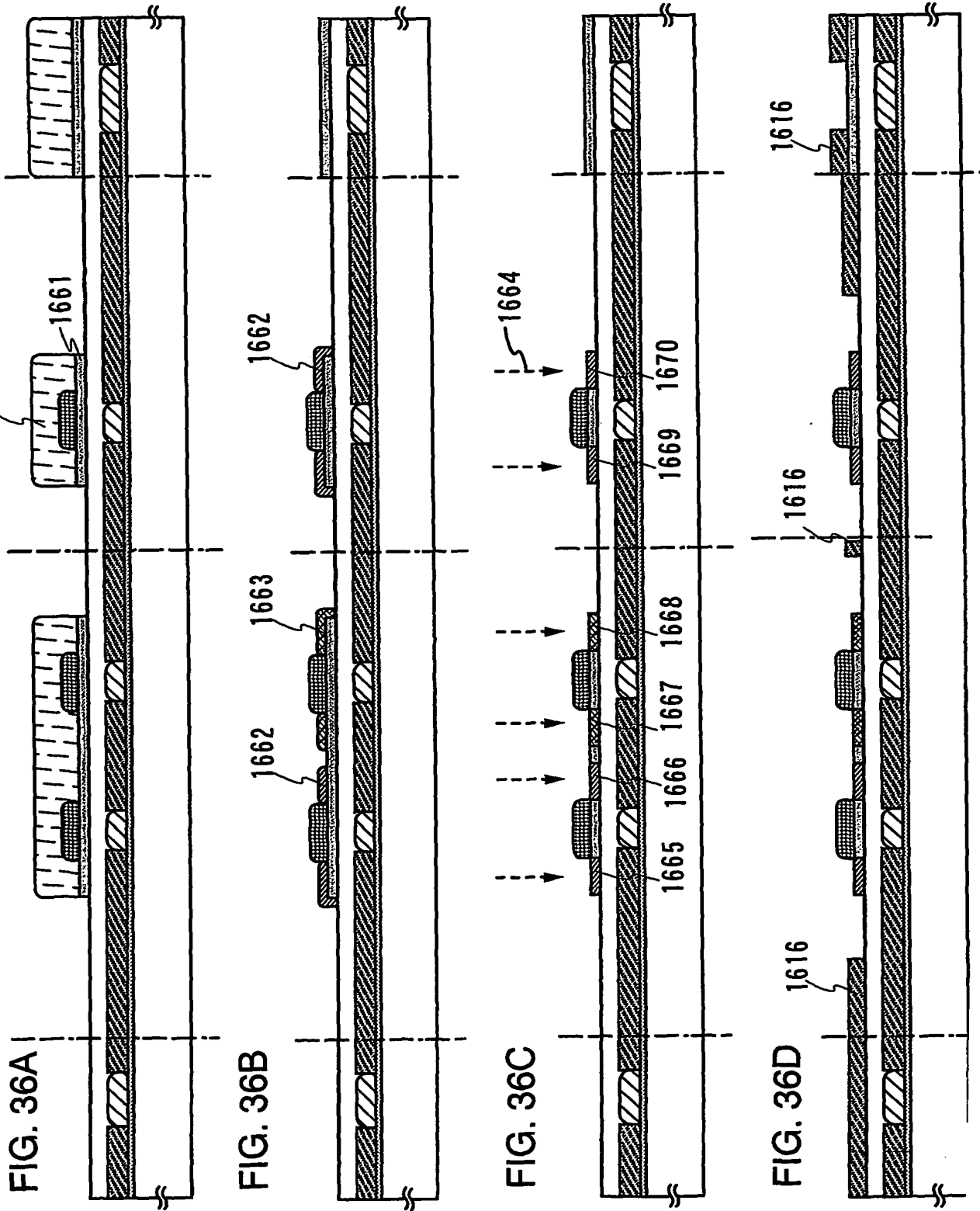


FIG. 35B





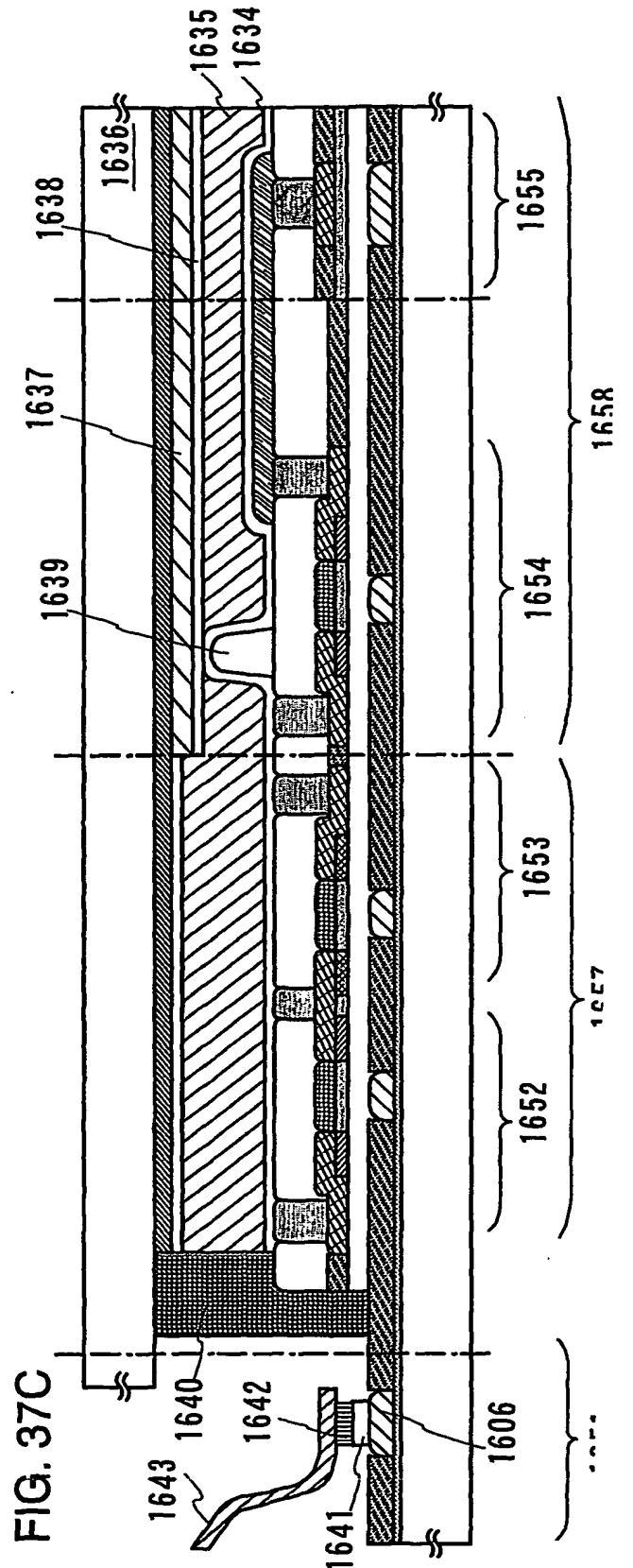
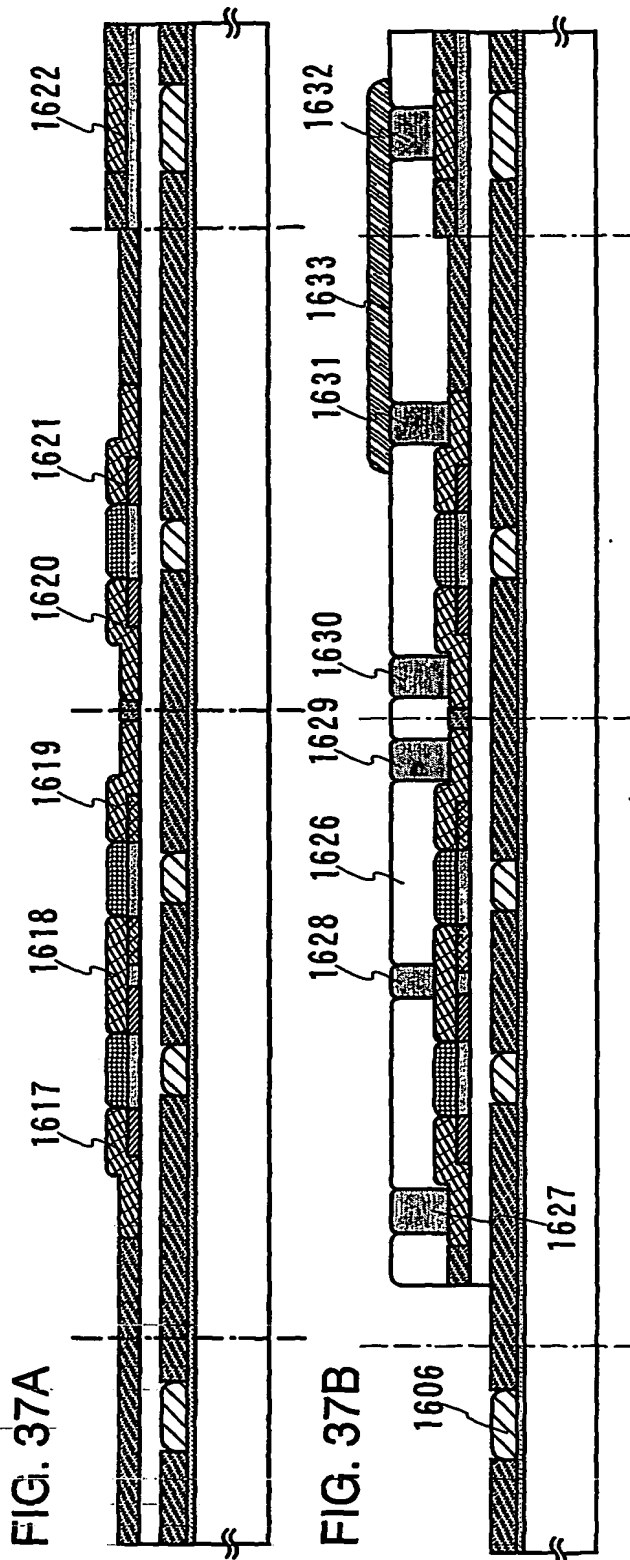


FIG. 38A

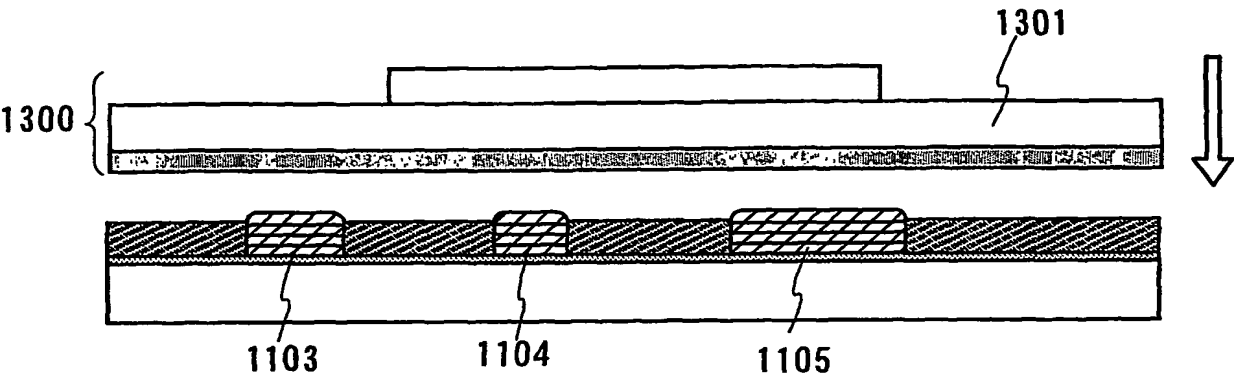


FIG. 38B

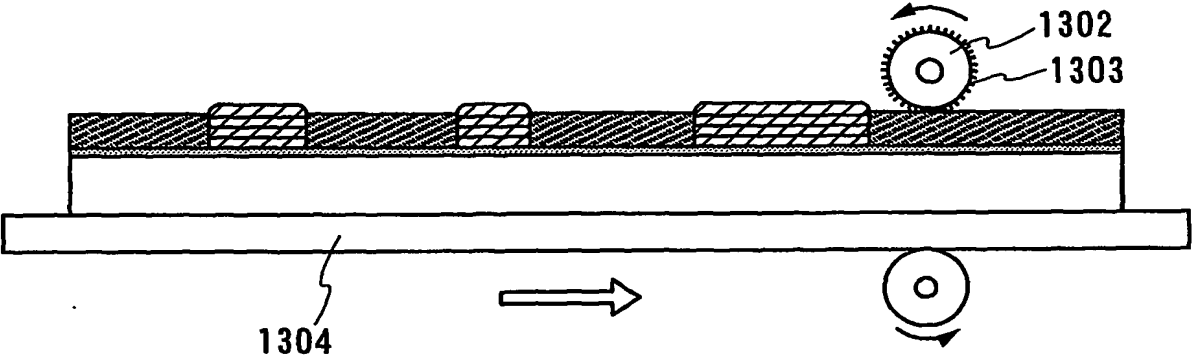
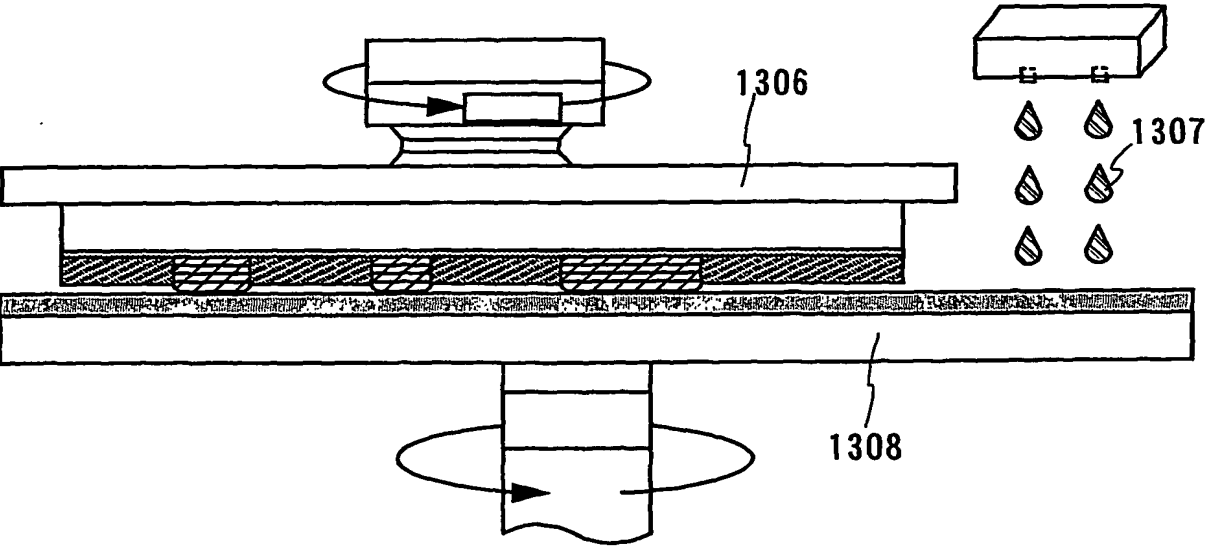


FIG. 38C



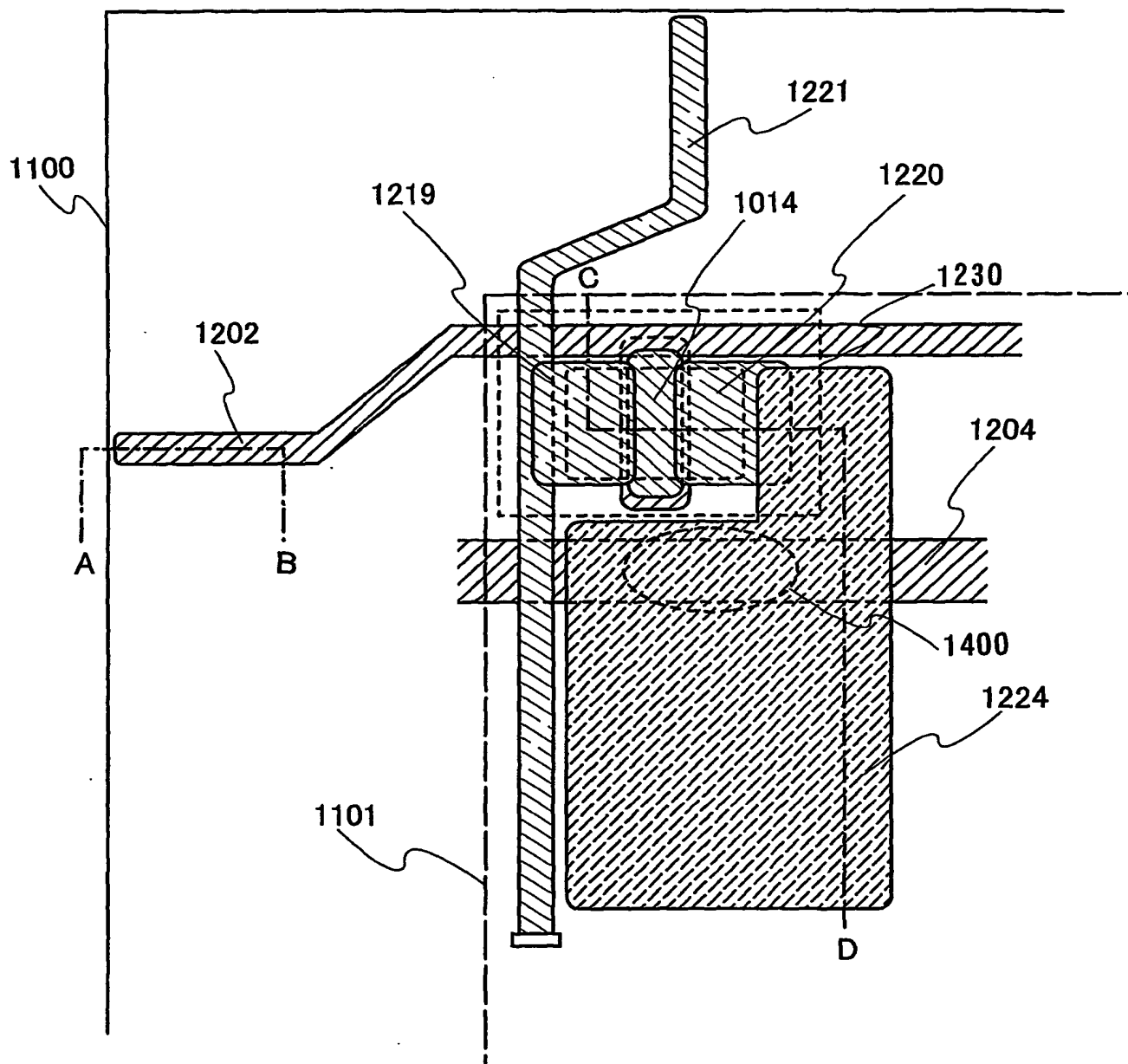


FIG. 39

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FIG. 40A

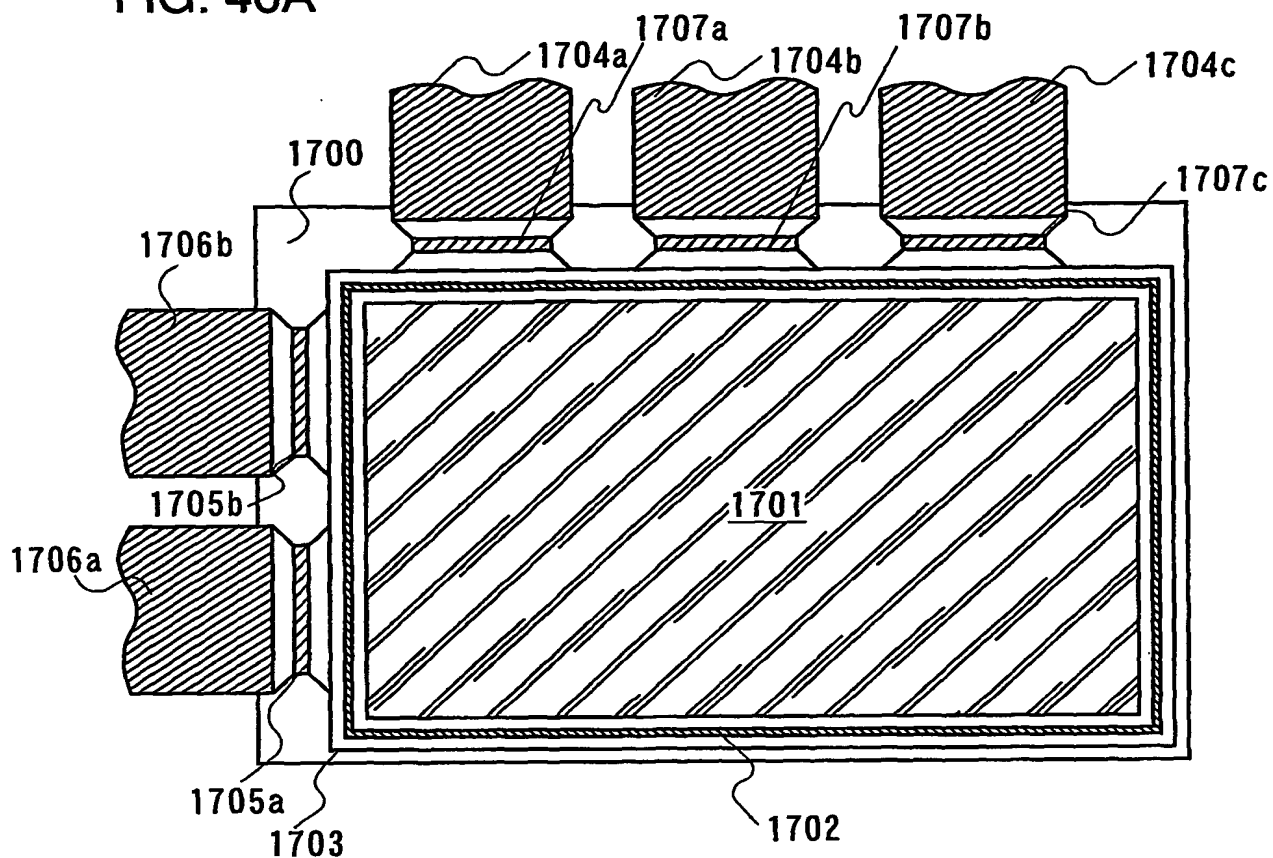
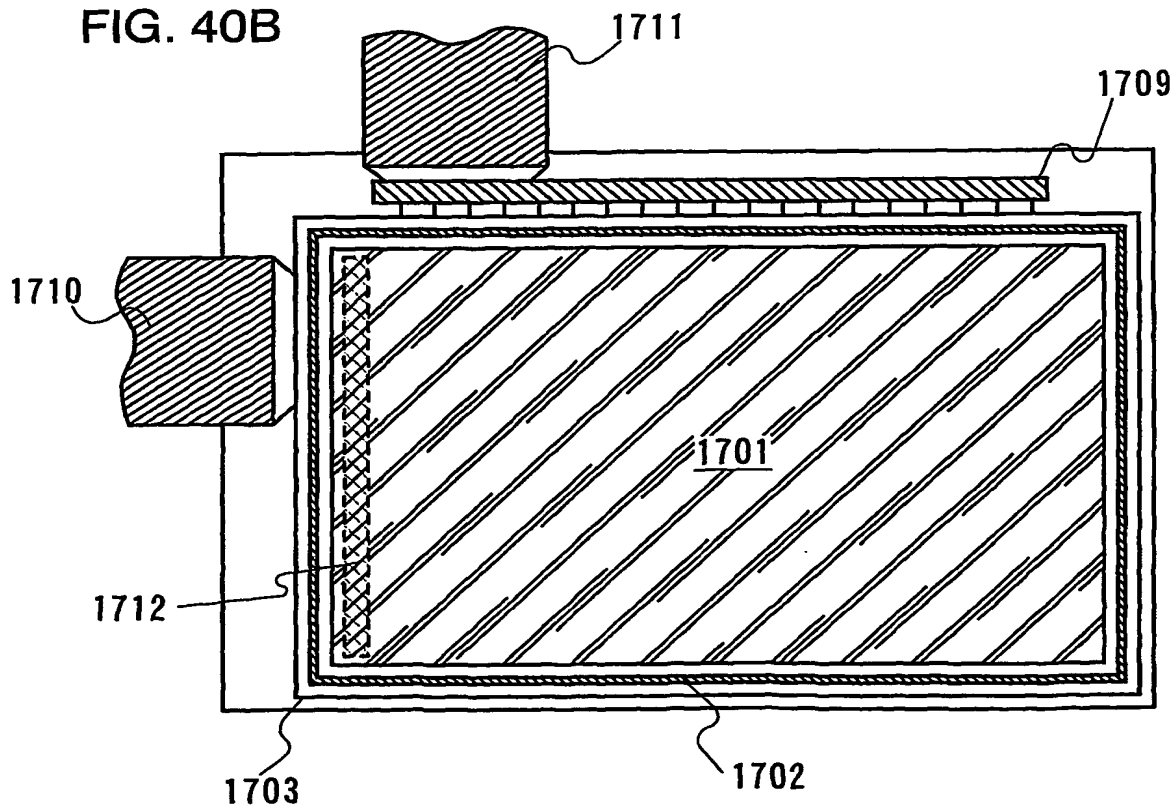


FIG. 40B



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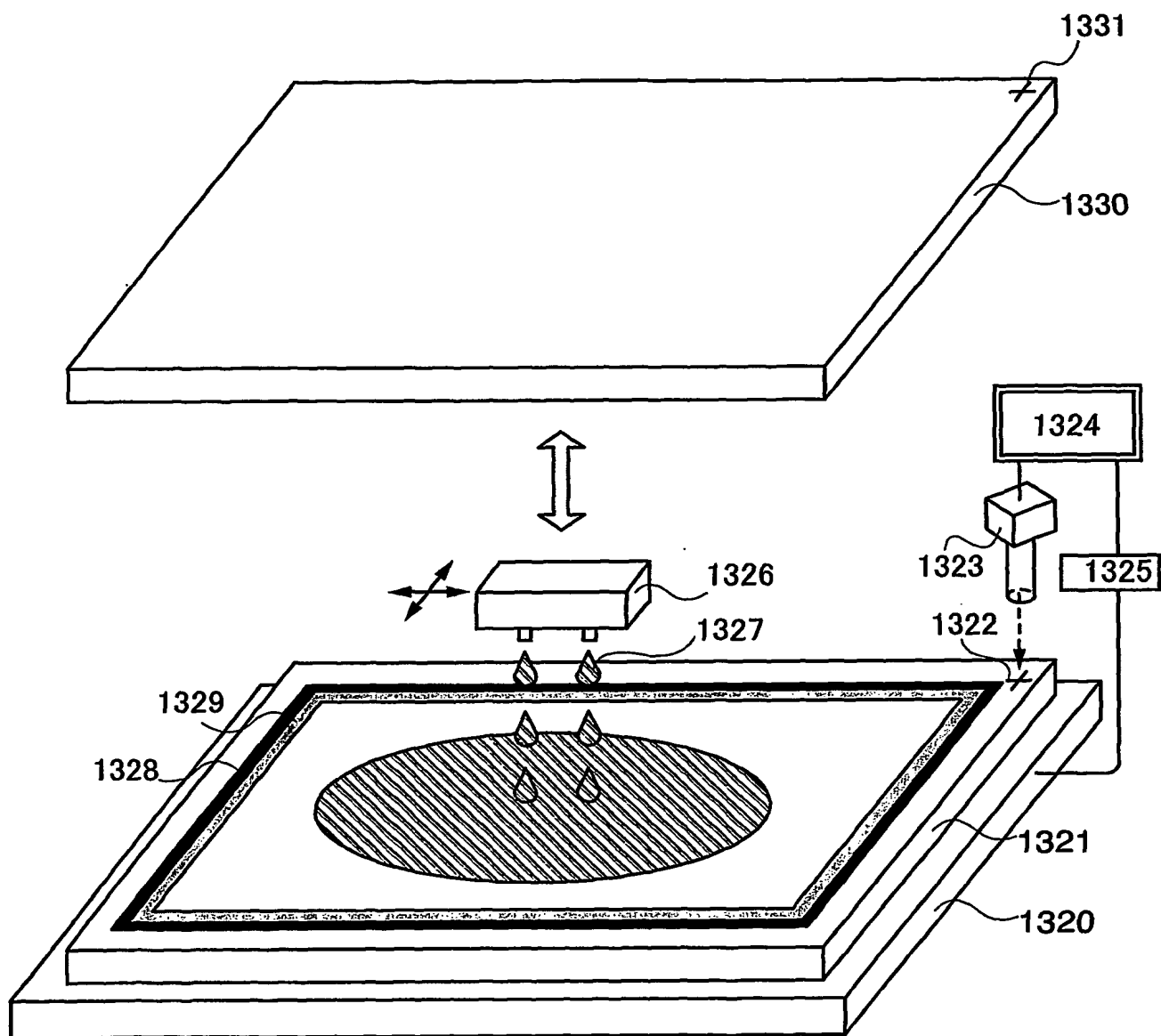


FIG. 41

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FIG. 43A

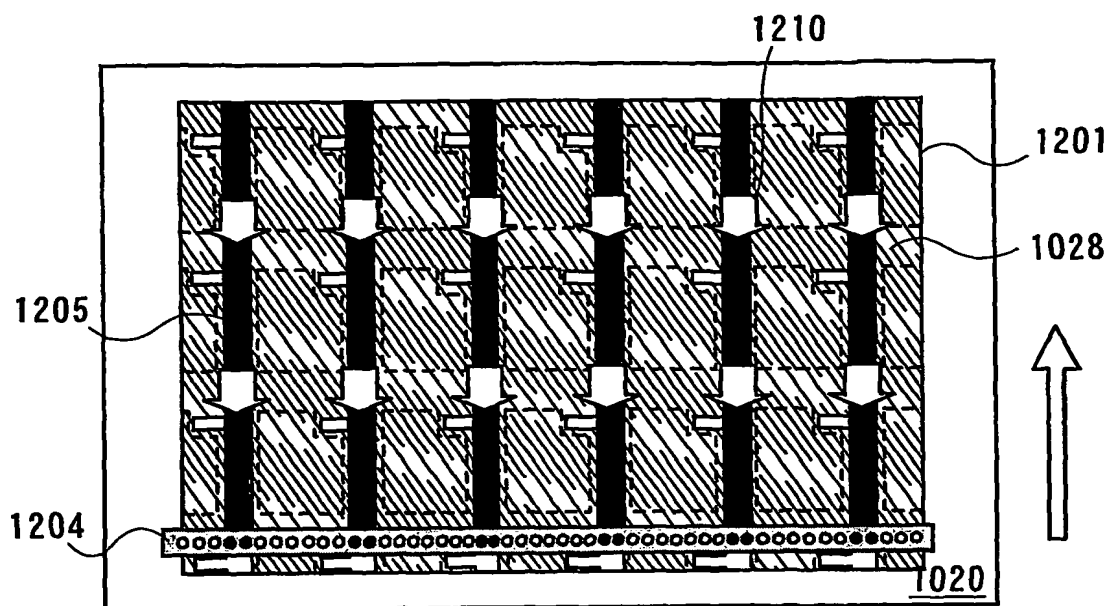
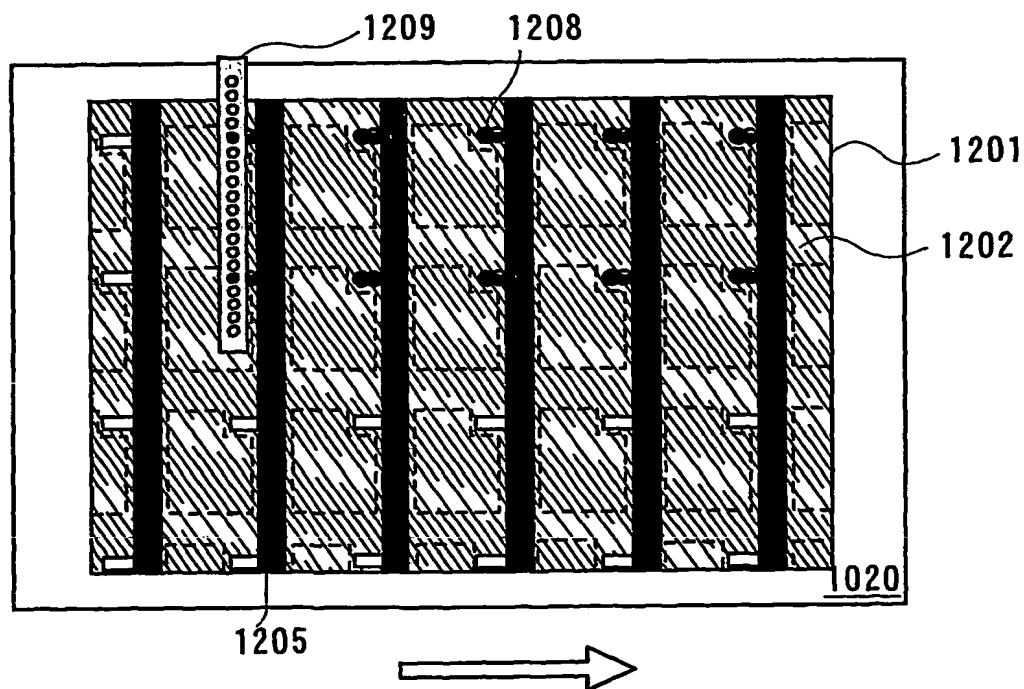


FIG. 43B



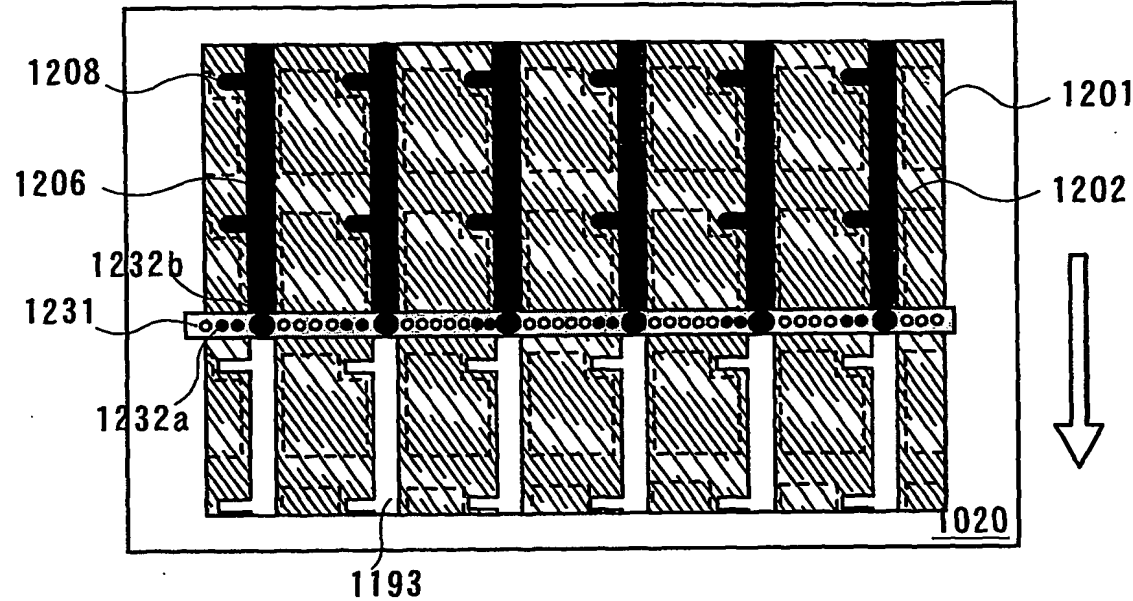


FIG. 44

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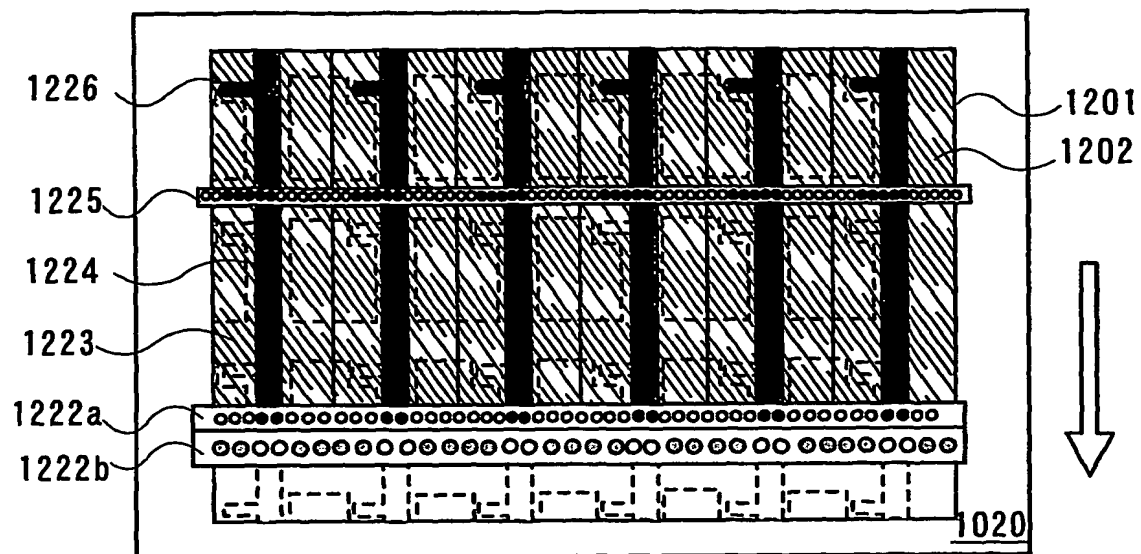


FIG. 45

FIG. 46A

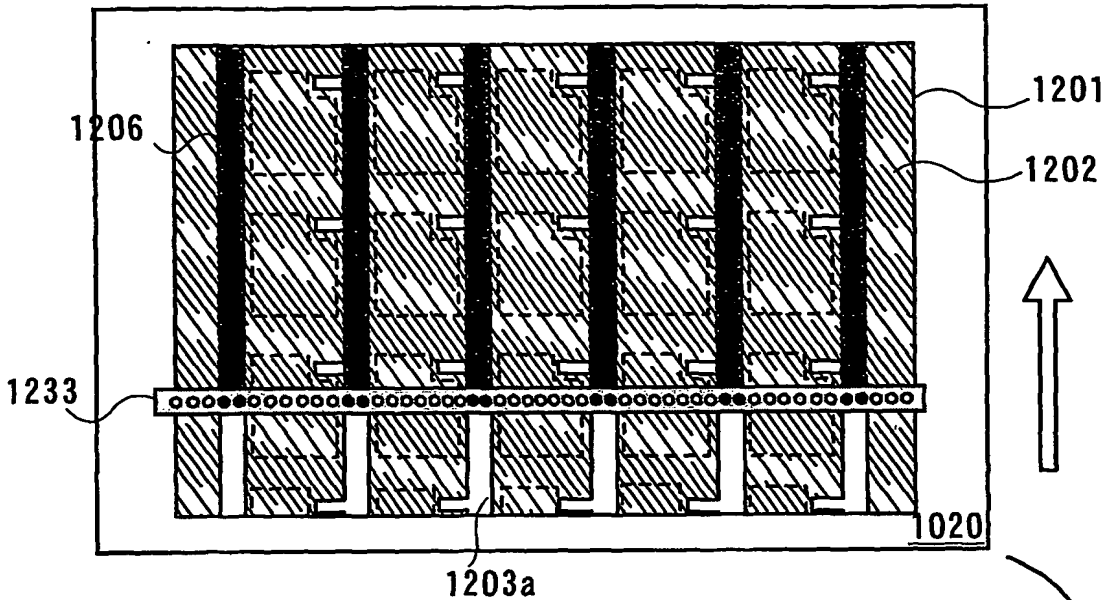
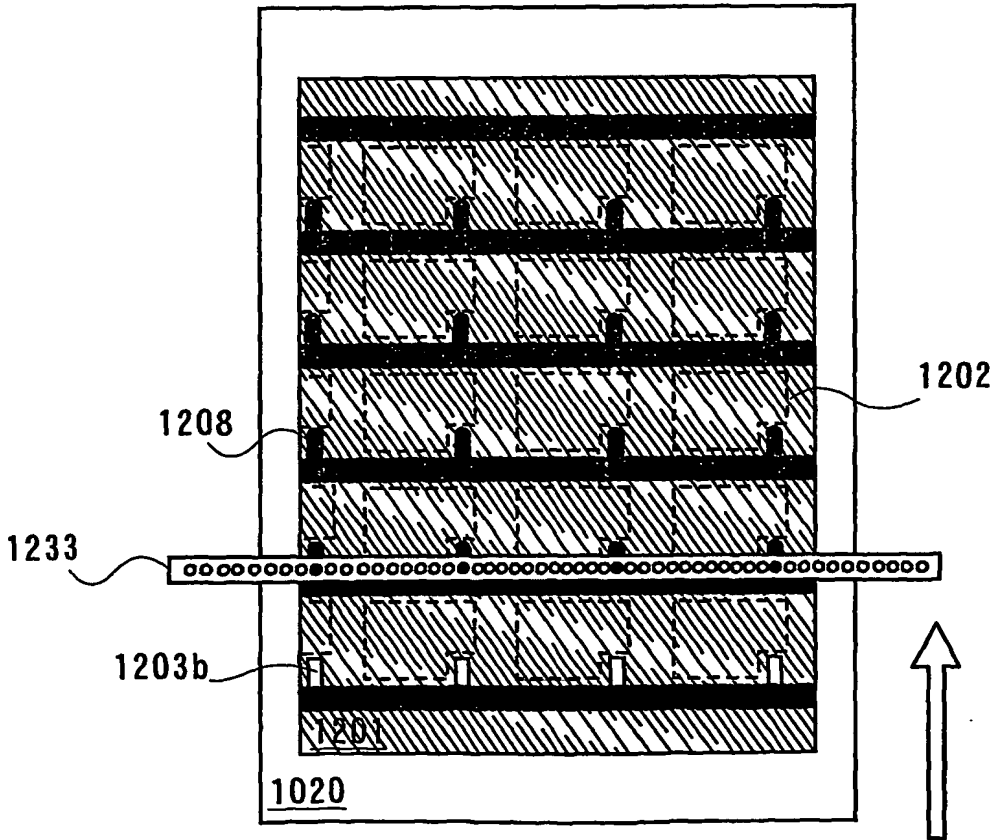


FIG. 46B



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FIG. 47A

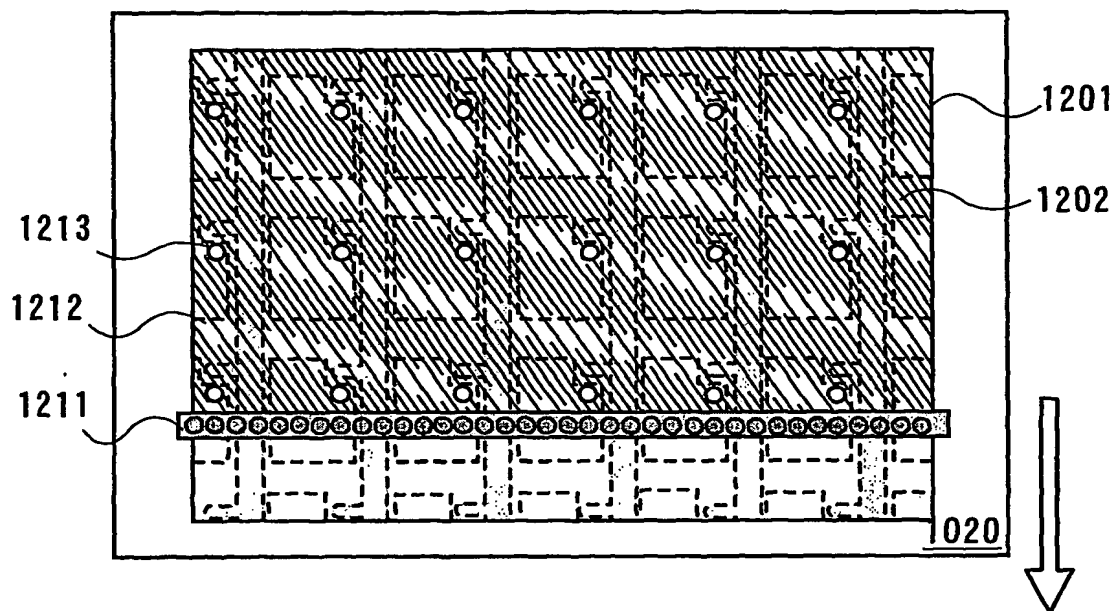


FIG. 47B

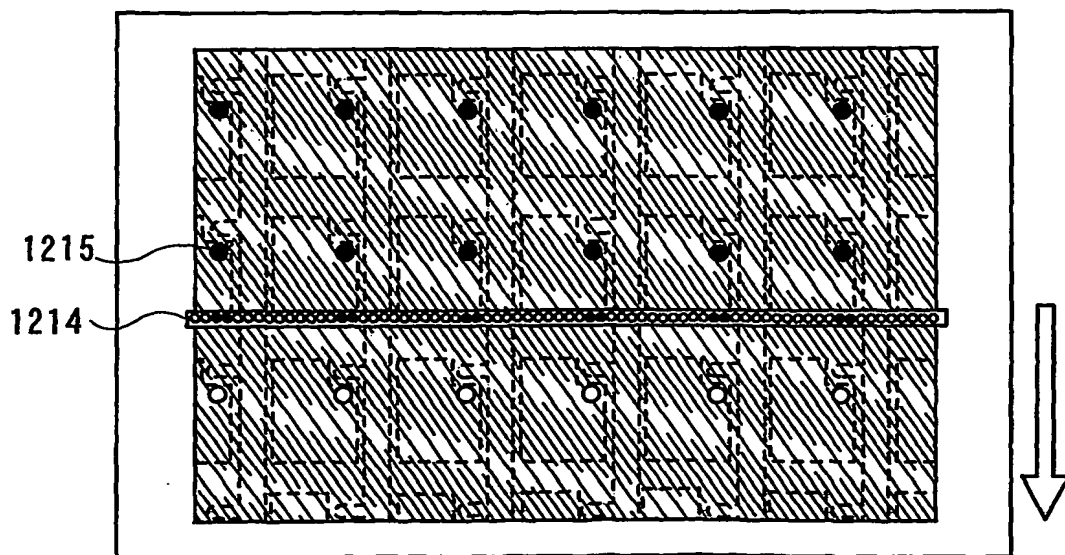


FIG. 48A

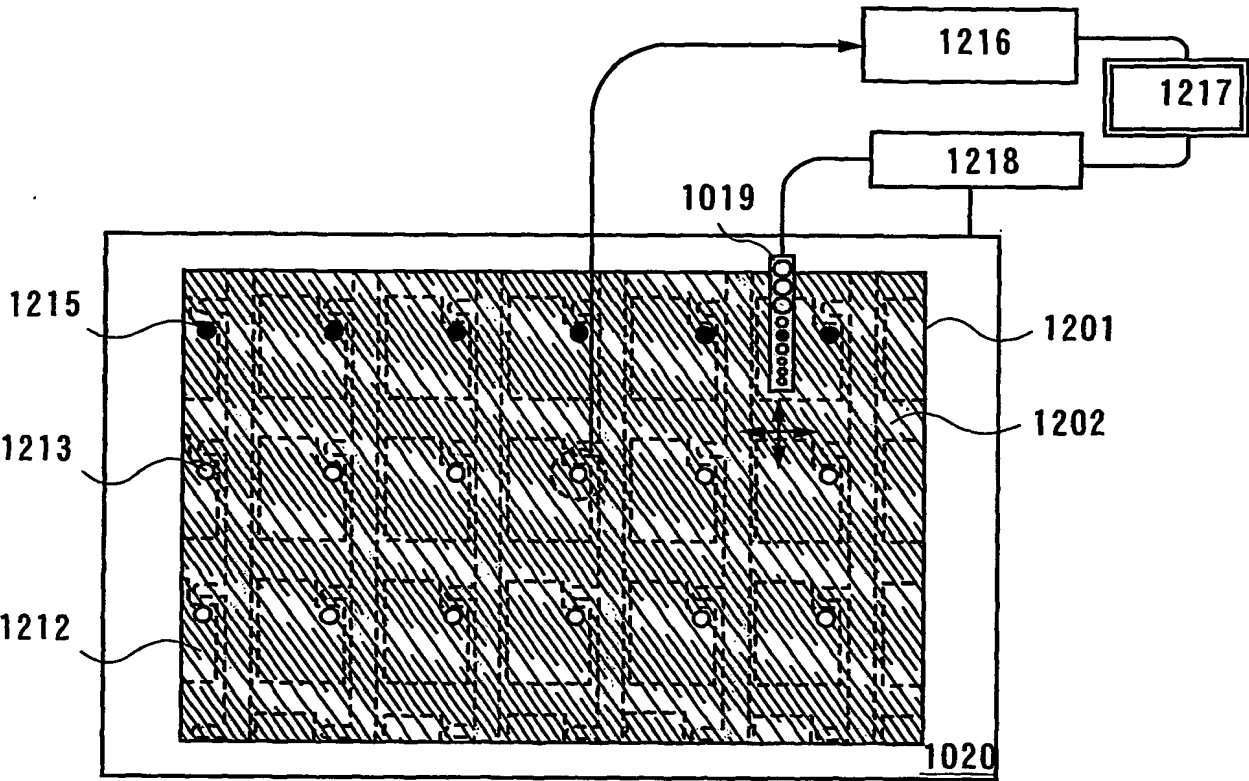
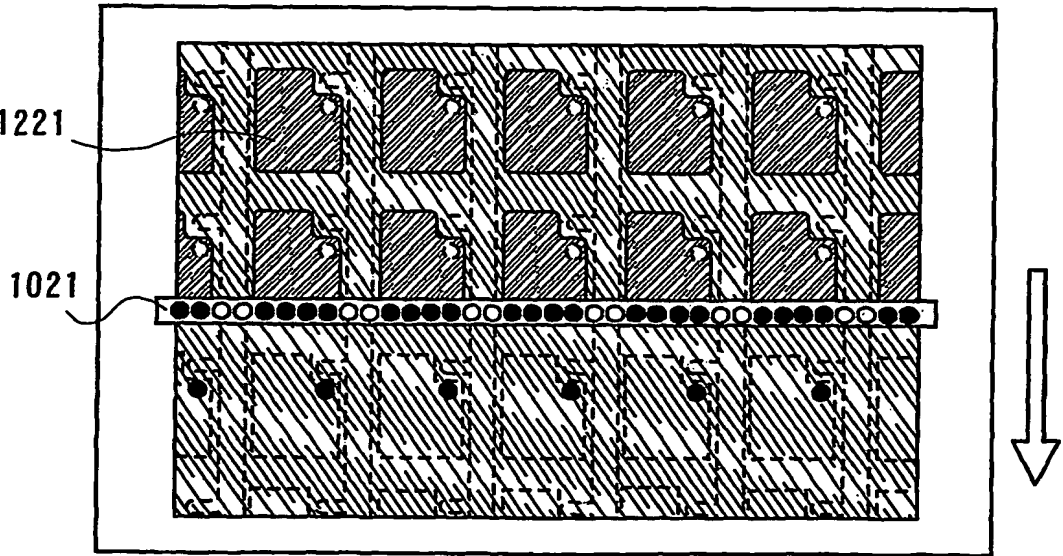


FIG. 48B



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FIG. 49A

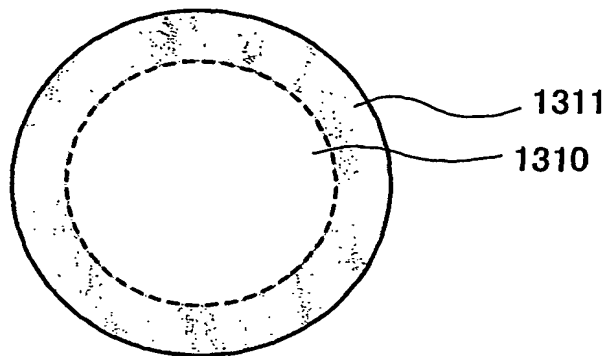


FIG. 49B

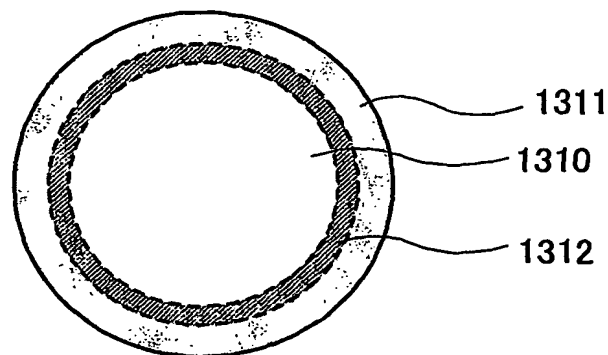


FIG. 50A

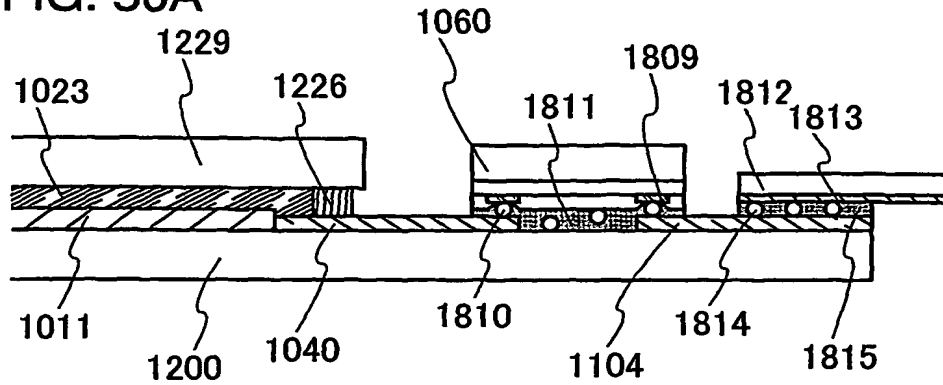
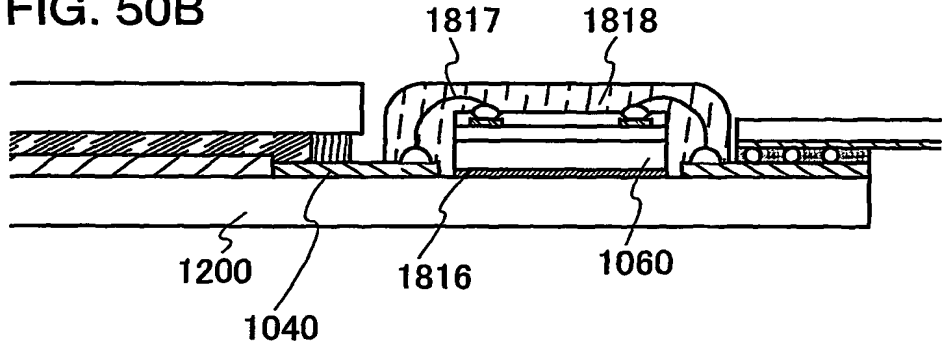


FIG. 50B



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FIG. 51A

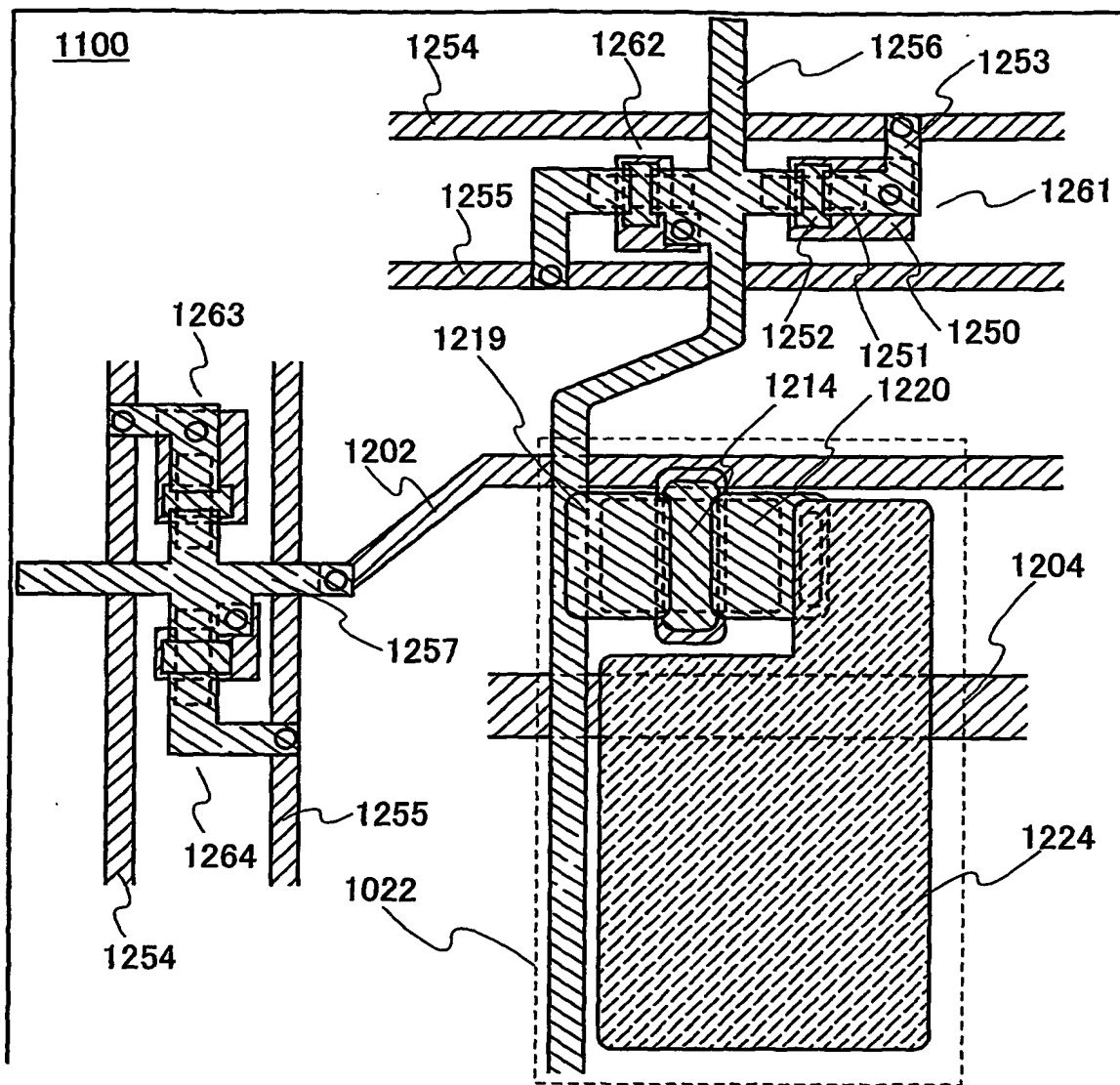
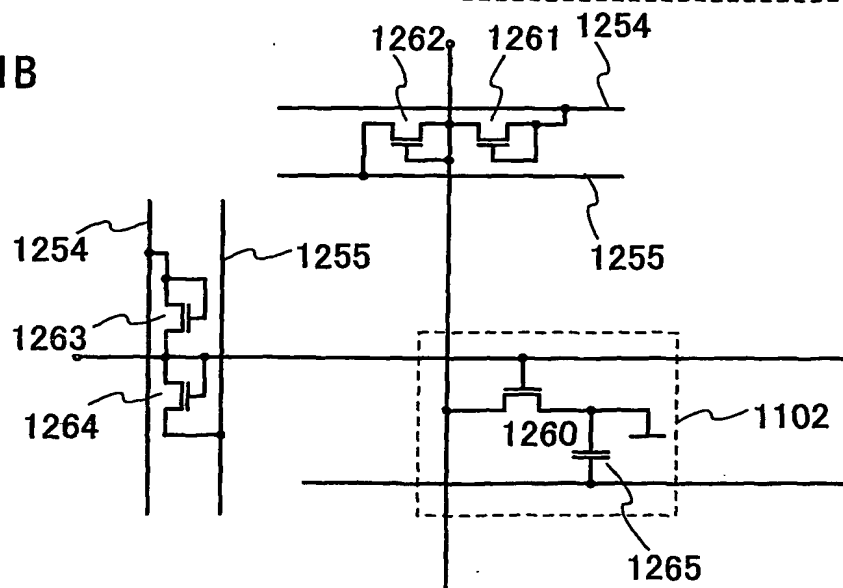


FIG. 51B



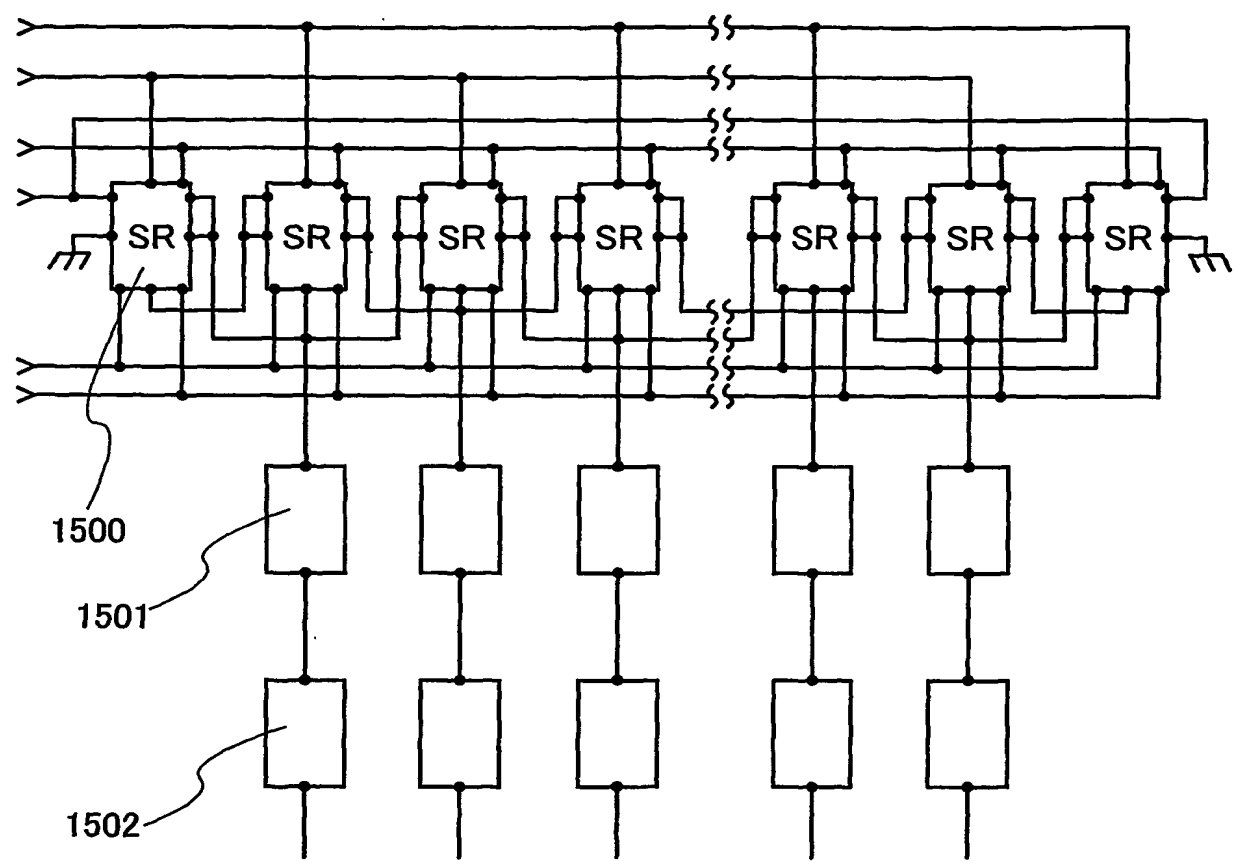


FIG. 52

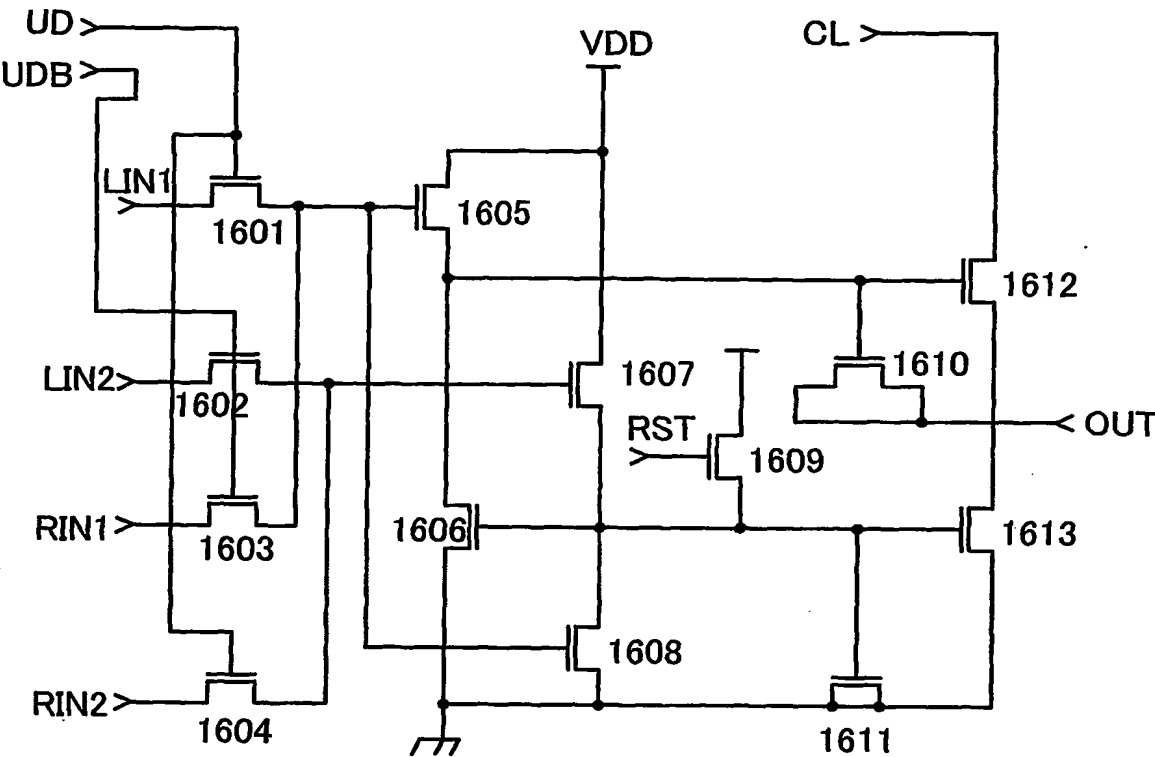


FIG. 53

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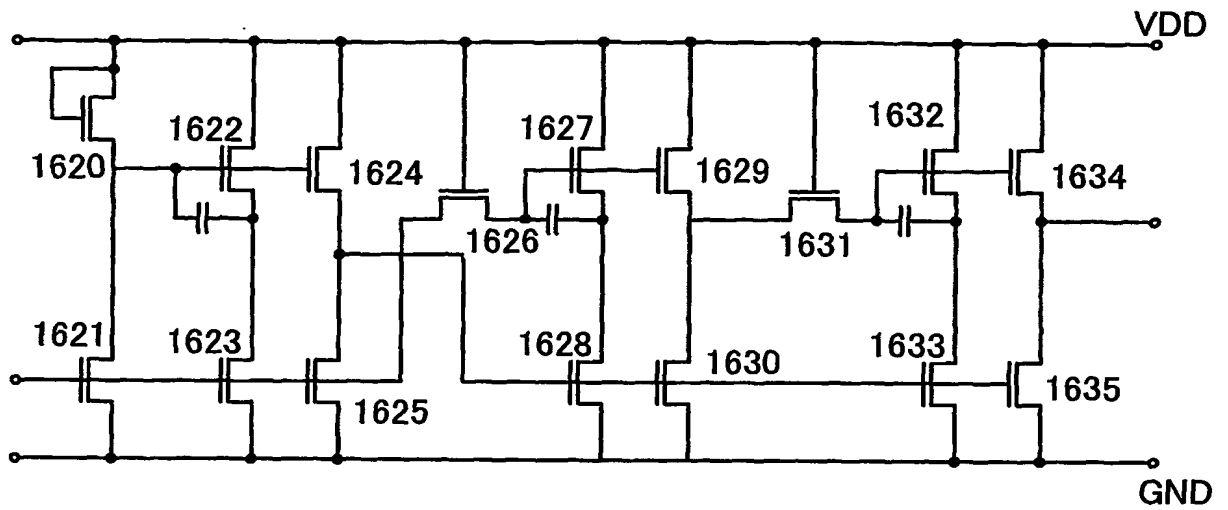


FIG. 54

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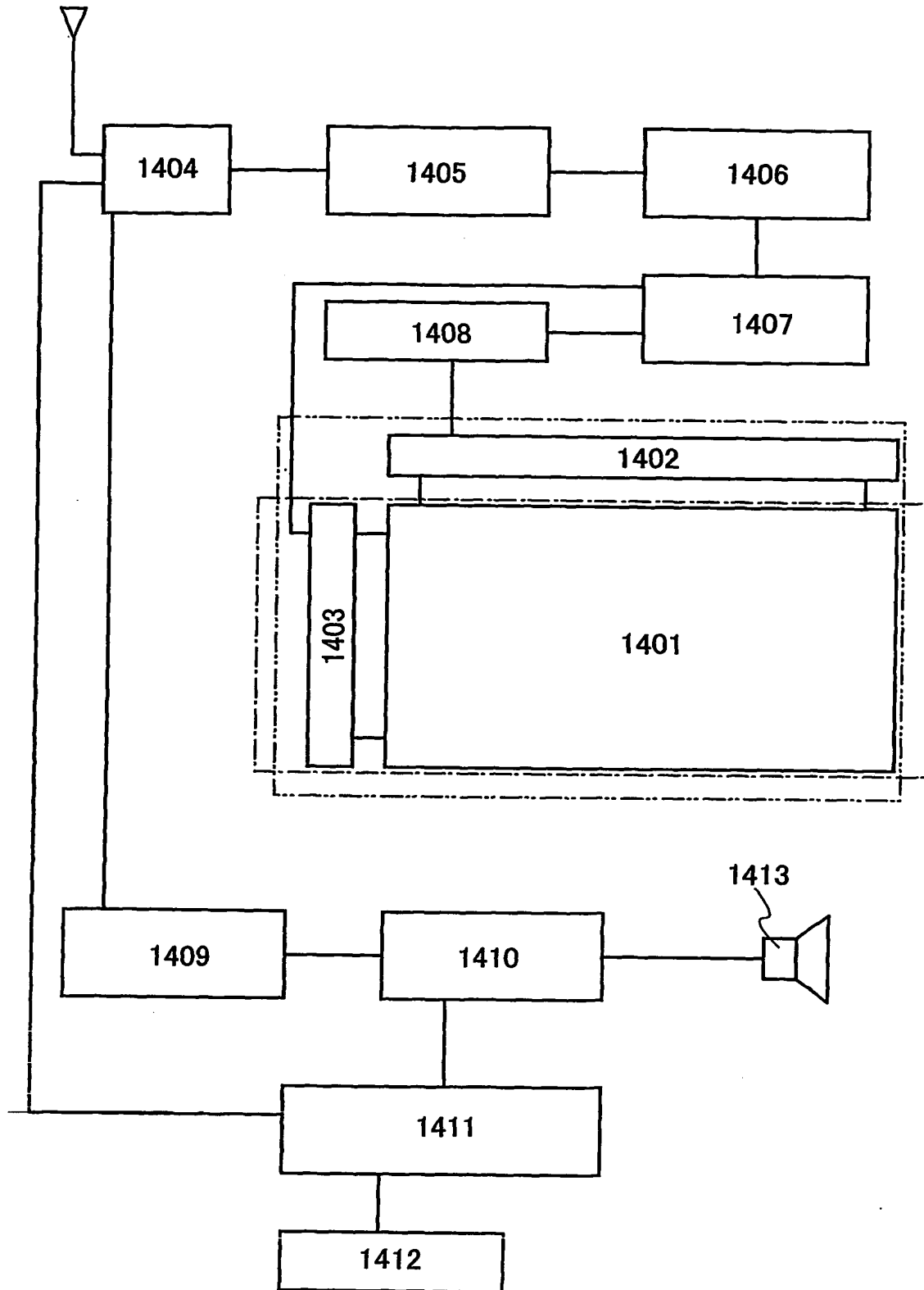


FIG. 55

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FIG. 56A

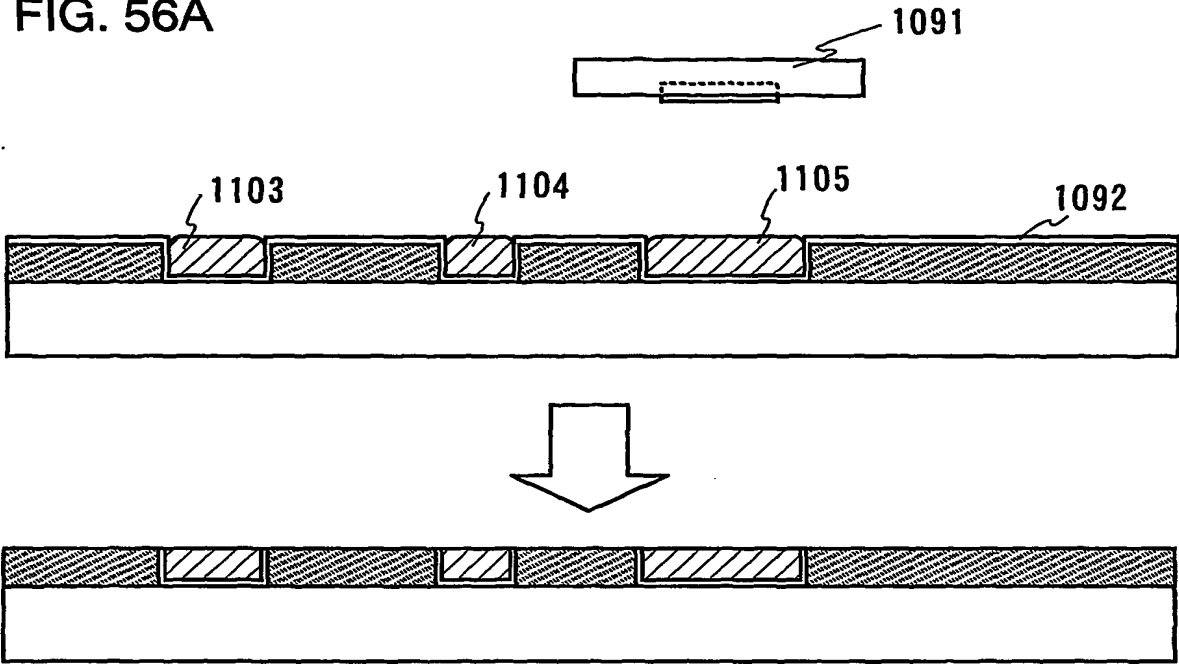
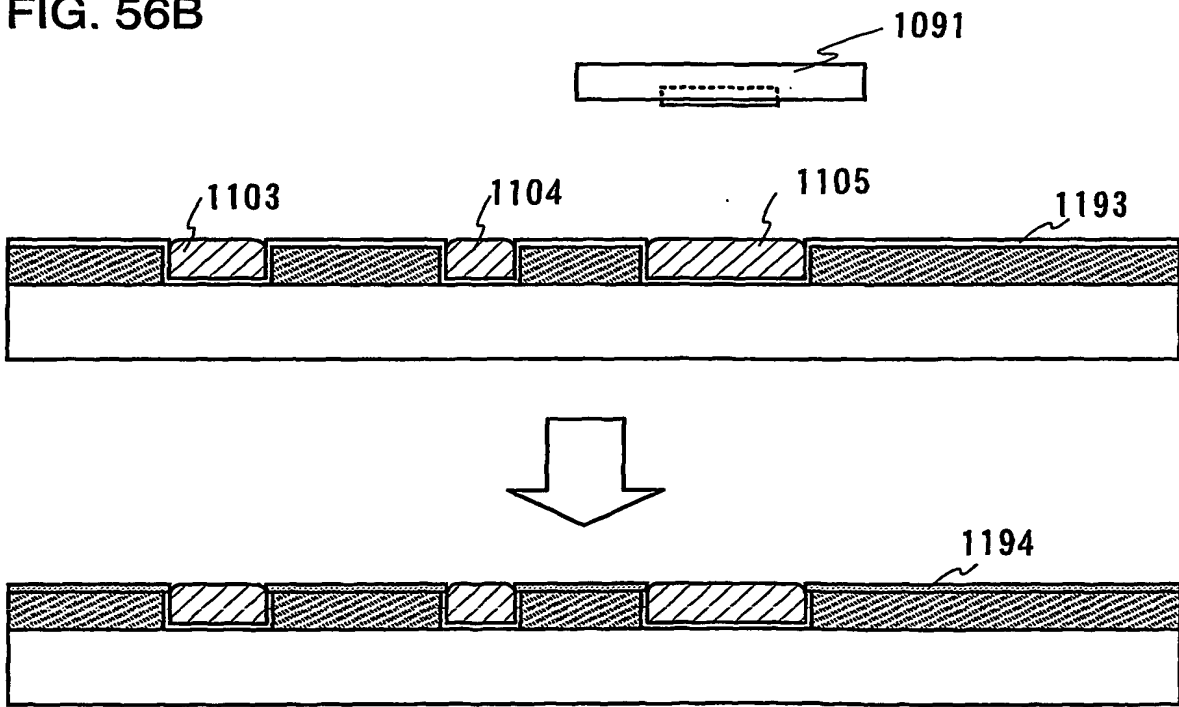


FIG. 56B



EXPLANATION OF REFERENCE

20 a depression, 21 a projection, 100 a substrate, 101 a base film, 102 an insulating film, 103 a conductive film, 103a a scan line, 103b a gate electrode, 104 a nozzle, 104a a nozzle, 105a a nozzle, 106 a gate insulating film, 107 an n-type semiconductor film, 108 a semiconductor film, 109 a conductive film, 109a a power line, 109b a drain electrode, 110 a switching TFT, 111 a driving TFT, 112 an insulating film, 112 a nozzle, 113 an interlayer insulating film, 114 a conductive film, 115 a pixel electrode, 118 an insulating film, 119 an electroluminescent layer, 120 an electrode, 130 an opening, 135 a color filter, 136 an insulating film, 140 an insulating film, 141 a resin, 151 a counter a substrate, 152 a desiccant, 153 a sealant, 155 a protective film, 160 an anisotropic conductive film, 161 an FPC, 162 an IC chip, 170 a substrate side, 171 a sealing substrate side, 173 an alignment film, 186 a liquid repellent material, 191 external light, 204 a nozzle, 230 nitrogen atmosphere, 401 a switching TFT, 402 a capacitor, 403 a driving TFT, 404 a current control TFT, 405 a light emitting element, 406 an erase TFT, 410 a signal line, 411 a power supply line, 412 a power supply line, 414 a scan line, 415 a scan line, 416 a scan line, 502 a pixel, 601a a control circuit, 602 a power supply circuit, 603 a pixel area, 604 a scan line driver circuit, 604a a scan line driver circuit, 605 a signal line driver circuit, 607 a printed circuit board, 608 a interfacing (I/F) unit, 609a a video signal processing circuit, 610a a VRAM, 611a an audio circuit, 650 a sealing a substrate, 651 a wave plate, 652 a wave plate, 653 a polarizer, 654 an anti-reflective film, 655 a storage capacitor, 803 a head, 803a a head, 803b a head, 805c a head, 804 an imaging means, 805 a droplet discharge means, 807 a control means, 808 a storage medium, 809 an image processing means, 810 a computer, 811 a marker, 830 an area, 831 a stage, 904 a tuner, 905 a video signal amplifier circuit, 906 a video signal processing circuit, 907 a control circuit, 908 a signal separation circuit, 909 an audio signal amplifier circuit, 910 an audio signal processing circuit, 911 a control circuit, 912 an input unit, 913 a speaker, 1000 a substrate, 1001 a titanium oxide film, 1002 a computer, 1003 a droplet discharge means, 1040 a signal line input terminal, 1004 a substrate, 1005 a imaging means, 1060 a driver IC, 1006 an image processing device, 1007 location information, 1008 a controller 1010 a base film, 1011 a pixel area, 1016 an XY è stage, 1017 an alignment marker, 1016 a stage, 1022 a pixel, 1023 a liquid crystal layer, 1100 a circuit design tool, 1101 data, 1102 an insulating layer, 1103 a gate wiring, 1104 a gate electrode, 1105 a capacitor wiring, 1106 a gate insulating film, 1107 a semiconductor film, 1108 a channel protective film, 1109 a

thin film pattern, 1109 an n-type semiconductor film, 1110 a nozzle, 1110 a resist mask, 1111 an insulating layer, 1112 a nozzle, 1112 a source electrode, 1113 a discharge opening, 1113 a drain electrode, 1114 a source region, 1115 a drain region, 1116 an insulating layer, 1117 a source wiring, 1118 a drain wiring, 1119 a counter substrate, 1120 a black matrix, 1121 a gate wiring, 1121 a color filter, 1122 a transparent resin, 1123 a counter electrode, 1124 an alignment film, 1125 a liquid crystal layer, 1126 a pixel electrode, 1127 a semiconductor island film, 1128 an n-type semiconductor island film, 1129 an alignment film, 1130 a metal mask, 1132 an insulating film, 1134 UV light, 1140 a polarizer, 1141 a backlight unit, 1142 a cold cathode tube (a fluorescent lamp), 1143 a lamp reflector, 1144 an optical waveguide, 1145 a diffuser plate, 1146 a reflector plate, 1152 a pixel electrode, 1153 a pixel electrode, 1160 an insulating layer, 1162 an insulating layer, 1170 a planarizing film, 1171 a source wiring, 1172 a drain wiring, 1173 an alignment film, 1174 a conductor, 1175 a planarizing film, 1178 an alignment film, 1179 an insulator, 1180 a planarizing film, 1182 a drain wiring, 1184 an alignment film, 1185 a passivation film, 1186 a liquid repellent material, 1187 a mask, 1189 a planarizing film, 1190 fluorescence, 1190 a drain wiring, 1191 external light, 1192 a nozzle, 1192 a pixel electrode, 1192 an alignment film, 1193 a titanium film, 1194 a titanium oxide film, 1200 a TFT substrate, 1201 a substrate, 1202 an insulating layer, 1202 a scan line, 1204 a continuous discharge nozzle, 1205 a wiring area, 1206 a wiring area, 1208 a electrode area, 1209 an intermittent discharge nozzle, 1210 UV light, 1211 a continuous discharge nozzle, 1214 an intermittent discharge nozzle, 1214 a gate electrode, 1216 a hole detection means, 1217 a CPU, 1218 a controller, 1019 a nozzle, 1219 a source electrode, 1219 a drain wiring layer, 1021 a nozzle, 1220 a drain electrode, 1221 a signal line, 1222 a continuous discharge nozzle, 1222a a nozzle, 1223b a nozzle, 1224 a pixel electrode, 1225 an intermittent discharge nozzle, 1226 sealant, 1229 a counter a substrate, 1230 a thin film transistor, 1231 a nozzle, 1232a an intermittent discharge opening, 1232b a continuous discharge opening, 1233 a stationary nozzle, 1250 a gate electrode layer, 1251 a semiconductor layer, 1252 an insulating layer, 1253 a wiring layer, 1254 a wiring, 1256 a signal wiring layer, 1260 a TFT, 1261 a protection diode, 1262 a TFT, 1300 a press mechanism, 1301 a heater, 1302 a roller, 1303 a micro brush, 1304 a stage, 1306 a wafer carrier, 1308 a polishing pad, 1310 Cu, 1311 Ag, 1312 a buffer layer, 1320 a stage, 1321 a substrate, 1322 an alignment marker, 1323 an imaging means, 1324 a CPU, 1325 a controller, 1326 a nozzle (a dispenser), 1327 a liquid crystal

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material, 1328 a sealant, 1329 a barrier layer, 1330 a substrate, 1401 a pixel area, 1402 a signal line driver circuit, 1403 a scan line driver circuit, 1404 a tuner, 1405 a video signal amplifier circuit, 1407 a control circuit, 1408 a signal dividing circuit, 1409 an audio signal amplifier circuit, 1410 an audio signal processing circuit, 1411 a control circuit, 1412 an input unit, 1413 a speaker, 1500 a pulse output circuit, 1501 a buffer circuit, 1600 a substrate, 1601 a titanium oxide, 1601 a TFT, 1602 an insulating layer, 1603a a gate electrode layer, 1604 a gate electrode layer, 1604 a capacitor electrode, 1606 a wiring, 1607 a gate insulating film, 1608 a semiconductor film, 1609 a channel protective film, 1611 a photoresist, 1612 an n-type semiconductor island film, 1614 a photoresist, 1615 a p-type semiconductor island film, 1616 an insulating layer, 1617 a drain electrode, 1620 a TFT, 1622 a counter electrode, 1626 an insulating layer, 1627 a drain wiring, 1628 an FPC, 1632 a capacitor wiring, 1633 a pixel electrode, 1634 an alignment film, 1635 a liquid crystal layer, 1636 a counter substrate, 1637 a transparent conductive film, 1638 an alignment film, 1639 a columnar spacer, 1640 a sealant, 1641 a terminal electrode, 1642 an anisotropic conductive film, 1643 an FPC, 1651 a terminal area, 1652 a drive circuit TFT, 1653 a p-channel TFT, 1654 a pixel TFT, 1655 a storage capacitor area, 1657 a driver circuit area, 1658 a pixel area, 1660 a photoresist, 1661 a semiconductor island film, 1662 a dopant source, 1663 a dopant source, 1664 laser, 1665 a drain region, 1700 a substrate, 1701 a pixel area, 1702 a sealant, 1703 a counter substrate, 1704a an FPC, 1705a a driver IC, 1706a an FPC, 1707a a driver IC, 1709 a source driver, 1710 an FPC, 1711 an FPC, 1712 a gate driver, 1809 an input-output terminal, 1810 a conductive particle, 1811 a resin, 1812 an FPC, 1813 a wiring, 1814 a conductive particle, 1815 a resin, 1816 an adhesive material, 1817 an Au wire, 1818 a sealing resin, 2001 a chassis, 2002 a support, 2003 a display area, 2004 a speaker unit, 2005 a video signal input terminal, 2101 a main body, 2102 a chassis, 2103 a display area, 2104 an audio input section, 2105 an audio output section, 2106 an operation key, 2107 an antenna, 2301 a main body, 2303 a display area, 2304 an audio input section, 2305 an audio output section, 2306 a switch, 2307 an external connection port, 2308 an earpiece, 2309 a touch panel operation key, 3101 a main body, 3102 a display area, 3104 a storage medium, 3105 an operation switch, 3106 an antenna.